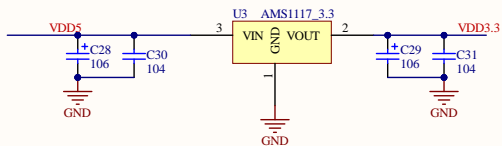
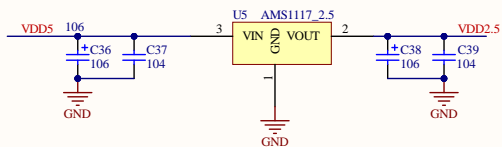


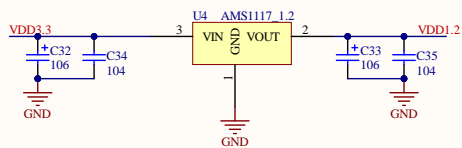
5V转数字3.3V电路



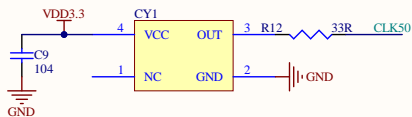
5V转2.5V电路



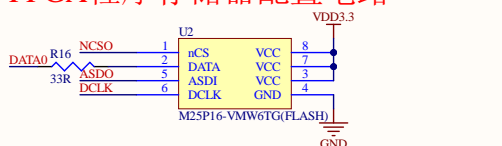
3.3V转数字1.2V电路



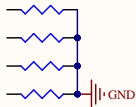
50MHZ晶振



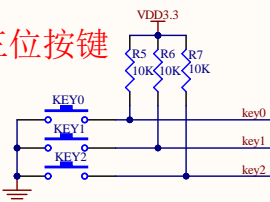
FPGA程序存储器配置电路



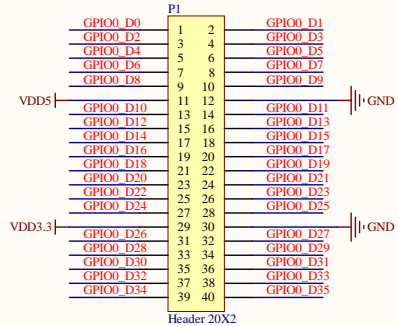
安装孔



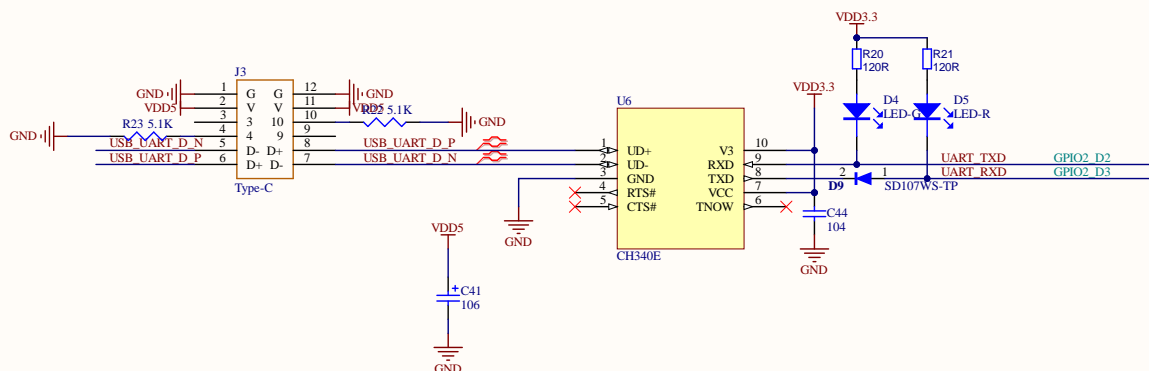
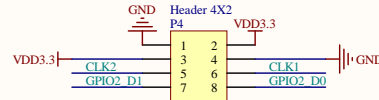
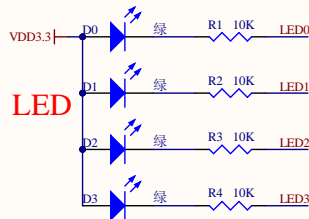
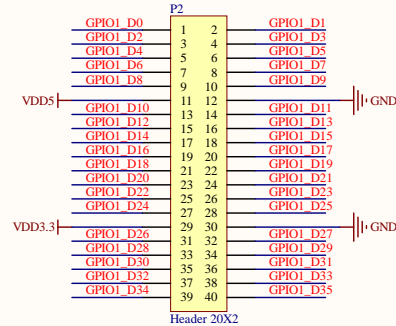
三位按键



GPIO 0



GPIO 1



Title		
Size	Number	Revision
A3		
Date:	11-18-2024	Sheet of
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