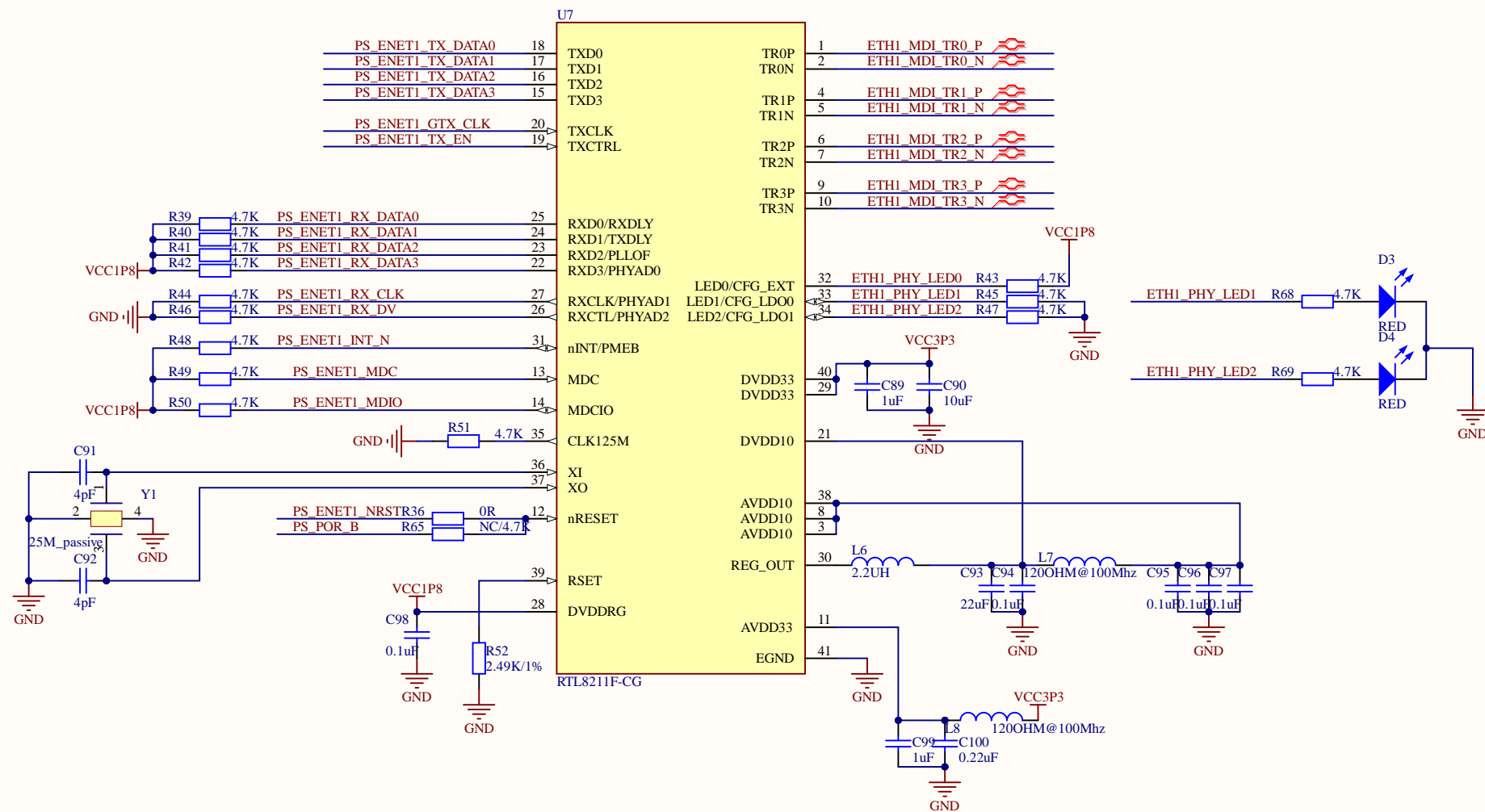
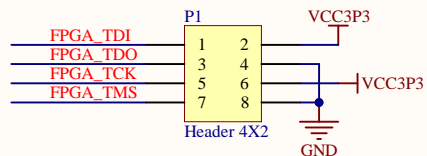
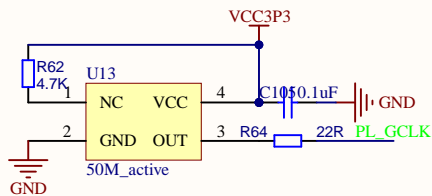
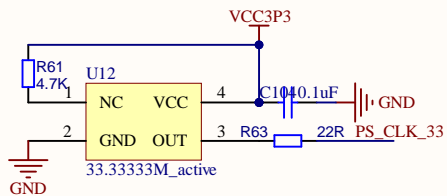
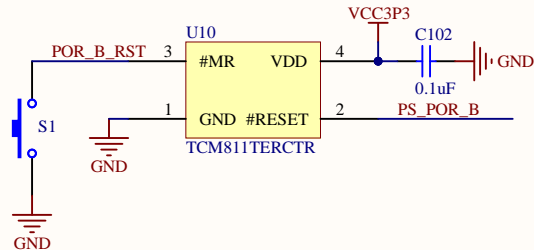
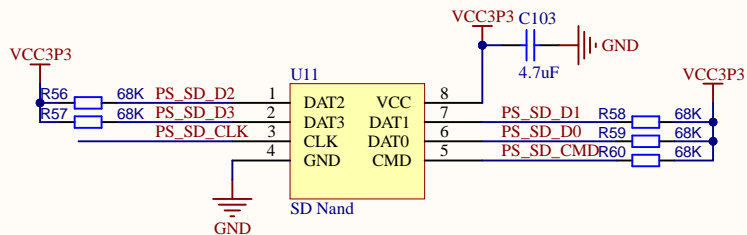
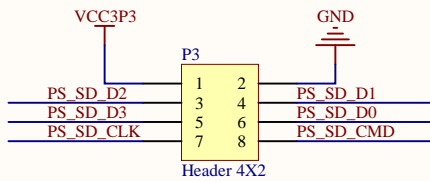
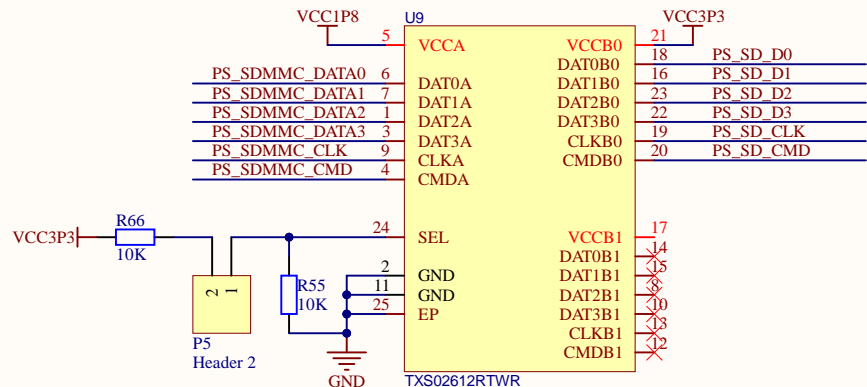
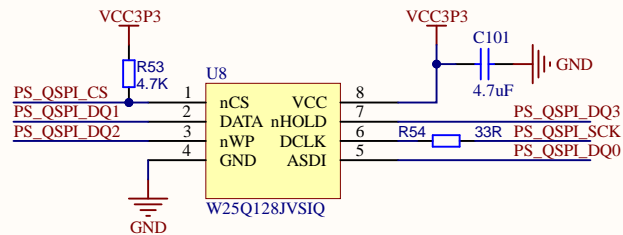


Title		
Size	Number	Revision
A4		
Date:	11-24-2023	Sheet of
File:	D:\AC608-7Z010\...\DDR3.SchDoc	Drawn By:



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如果你需要使用邮票孔上的59号管脚作为普通用户IO使用，则需要将核心板上的50MHz有源晶振取下，然后使用PS为FPGA提供工作时钟。

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U1A

BANK 13	IO_L6N_T0_VREF_13	V5	PL_GPIO0
	IO_L11P_T1_SRCC_13	U7	PL_GPIO1
	IO_L11N_T1_SRCC_13	V7	PL_GPIO2
	IO_L12P_T1_MRCC_13	V9	PL_CLK0
	IO_L12N_T1_MRCC_13	U10	PL_GPIO3
	IO_L13P_T2_MRCC_13	V7	PL_CLK1
	IO_L13N_T2_MRCC_13	V6	PL_GPIO4
	IO_L14P_T2_SRCC_13	V9	PL_GPIO5
	IO_L14N_T2_SRCC_13	V8	PL_GPIO6
	IO_L15P_T2_DQS_13	V8	PL_GPIO7
	IO_L15N_T2_DQS_13	W8	PL_GPIO8
	IO_L16P_T2_13	W10	PL_GPIO9
	IO_L16N_T2_13	W9	PL_GPIO10
	IO_L17P_T2_13	U9	PL_GPIO11
	IO_L17N_T2_13	U8	PL_GPIO12
	IO_L18P_T2_13	W11	PL_GPIO13
	IO_L18N_T2_13	Y11	PL_GPIO14
	IO_L19P_T3_13	T5	PL_GPIO15
	IO_L19N_T3_VREF_13	U5	PL_GPIO16
	IO_L20P_T3_13	Y12	PL_GPIO17
	IO_L20N_T3_13	Y13	PL_GPIO18
	IO_L21P_T3_DQS_13	V11	PL_GPIO19
	IO_L21N_T3_DQS_13	V10	PL_GPIO20
	IO_L22P_T3_13	V6	PL_GPIO21
	IO_L22N_T3_13	W6	PL_GPIO22

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为兼容7010，本bank管脚全部不使用

U1B

BANK 34	IO_0_34	R19	PL_GPIO23
	IO_L1P_T0_34	T11	B34_L1_P
	IO_L1N_T0_34	T10	B34_L1_N
	IO_L2P_T0_34	T12	B34_L2_P
	IO_L2N_T0_34	U12	B34_L2_N
	IO_L3P_T0_DQS_PUDC_B_34	U13	B34_L3_P
	IO_L3N_T0_DQS_34	V13	B34_L3_N
	IO_L4P_T0_34	V12	B34_L4_P
	IO_L4N_T0_34	W13	B34_L4_N
	IO_L5P_T0_34	T14	B34_L5_P
	IO_L5N_T0_34	T15	B34_L5_N
	IO_L6P_T0_34	P14	B34_L6_P
	IO_L6N_T0_VREF_34	R14	B34_L6_N
	IO_L7P_T1_34	Y16	B34_L7_P
	IO_L7N_T1_34	Y17	B34_L7_N
	IO_L8P_T1_34	W14	B34_L8_P
	IO_L8N_T1_34	Y14	B34_L8_N
	IO_L9P_T1_DQS_34	T16	B34_L9_P
	IO_L9N_T1_DQS_34	U17	B34_L9_N
	IO_L10P_T1_34	V15	B34_L10_P
	IO_L10N_T1_34	W15	B34_L10_N
	IO_L11P_T1_SRCC_34	U14	B34_L11_P
	IO_L11N_T1_SRCC_34	U15	B34_L11_N
	IO_L12P_T1_MRCC_34	U18	PL_GCLK
	IO_L12N_T1_MRCC_34	U19	PL_GPIO46
	IO_L13P_T2_MRCC_34	N18	PL_CLK2
	IO_L13N_T2_MRCC_34	P19	PL_GPIO47
	IO_L14P_T2_SRCC_34	N20	B34_L14_P
	IO_L14N_T2_SRCC_34	P20	B34_L14_N
	IO_L15P_T2_DQS_34	T20	B34_L15_P
	IO_L15N_T2_DQS_34	U20	B34_L15_N
	IO_L16P_T2_34	V20	B34_L16_P
	IO_L16N_T2_34	W20	B34_L16_N
	IO_L17P_T2_34	Y18	B34_L17_P
	IO_L17N_T2_34	Y19	B34_L17_N
	IO_L18P_T2_34	V16	B34_L18_P
	IO_L18N_T2_34	W16	B34_L18_N
	IO_L19P_T3_34	R16	B34_L19_P
	IO_L19N_T3_VREF_34	R17	B34_L19_N
	IO_L20P_T3_34	T17	B34_L20_P
	IO_L20N_T3_34	R18	B34_L20_N
	IO_L21P_T3_DQS_34	V17	B34_L21_P
	IO_L21N_T3_DQS_34	V18	B34_L21_N
	IO_L22P_T3_34	W18	B34_L22_P
	IO_L22N_T3_34	W19	B34_L22_N
	IO_L23P_T3_34	N17	B34_L23_P
	IO_L23N_T3_34	P18	B34_L23_N
	IO_L24P_T3_34	P15	B34_L24_P
	IO_L24N_T3_34	P16	B34_L24_N
	IO_25_34	T19	PL_GPIO70

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U1C

BANK 35	IO_0_35	G14	PL_GPIO71
	IO_L1P_T0_AD0P_35	C20	B35_L1_P
	IO_L1N_T0_AD0N_35	B20	B35_L1_N
	IO_L2P_T0_AD8P_35	B19	B35_L2_P
	IO_L2N_T0_AD8N_35	A20	B35_L2_N
	IO_L3P_T0_DQS_AD1P_35	E17	B35_L3_P
	IO_L3N_T0_DQS_AD1N_35	D18	B35_L3_N
	IO_L4P_T0_35	D19	B35_L4_P
	IO_L4N_T0_35	D20	B35_L4_N
	IO_L5P_T0_AD9P_35	E18	B35_L5_P
	IO_L5N_T0_AD9N_35	E19	B35_L5_N
	IO_L6P_T0_35	F16	B35_L6_P
	IO_L6N_T0_VREF_35	F17	B35_L6_N
	IO_L7P_T1_AD2P_35	M19	B35_L7_P
	IO_L7N_T1_AD2N_35	M20	B35_L7_N
	IO_L8P_T1_AD10P_35	M17	B35_L8_P
	IO_L8N_T1_AD10N_35	M18	B35_L8_N
	IO_L9P_T1_DQS_AD3P_35	L19	B35_L9_P
	IO_L9N_T1_DQS_AD3N_35	L20	B35_L9_N
	IO_L10P_T1_AD11P_35	K19	B35_L10_P
	IO_L10N_T1_AD11N_35	J19	B35_L10_N
	IO_L11P_T1_SRCC_35	L16	B35_L11_P
	IO_L11N_T1_SRCC_35	L17	B35_L11_N
	IO_L12P_T1_MRCC_35	K17	PL_CLK3
	IO_L12N_T1_MRCC_35	K18	PL_GPIO94
	IO_L13P_T2_MRCC_35	H16	PL_CLK4
	IO_L13N_T2_MRCC_35	H17	PL_GPIO95
	IO_L14P_T2_AD4P_SRCC_35	J18	B35_L14_P
	IO_L14N_T2_AD4N_SRCC_35	H18	B35_L14_N
	IO_L15P_T2_DQS_AD12P_35	F19	B35_L15_P
	IO_L15N_T2_DQS_AD12N_35	F20	B35_L15_N
	IO_L16P_T2_35	G17	B35_L16_P
	IO_L16N_T2_35	G18	B35_L16_N
	IO_L17P_T2_AD5P_35	J20	B35_L17_P
	IO_L17N_T2_AD5N_35	H20	B35_L17_N
	IO_L18P_T2_AD13P_35	G19	B35_L18_P
	IO_L18N_T2_AD13N_35	G20	B35_L18_N
	IO_L19P_T3_35	H15	B35_L19_P
	IO_L19N_T3_VREF_35	G15	B35_L19_N
	IO_L20P_T3_AD6P_35	K14	B35_L20_P
	IO_L20N_T3_AD6N_35	J14	B35_L20_N
	IO_L21P_T3_DQS_AD14P_35	N15	B35_L21_P
	IO_L21N_T3_DQS_AD14N_35	N16	B35_L21_N
	IO_L22P_T3_AD7P_35	L14	B35_L22_P
	IO_L22N_T3_AD7N_35	L15	B35_L22_N
	IO_L23P_T3_35	M14	B35_L23_P
	IO_L23N_T3_35	M15	B35_L23_N
	IO_L24P_T3_AD15P_35	K16	B35_L24_P
	IO_L24N_T3_AD15N_35	J16	B35_L24_N
	IO_25_35	J15	PL_GPIO118

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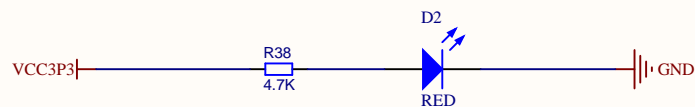
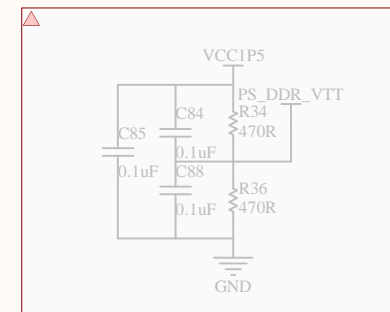
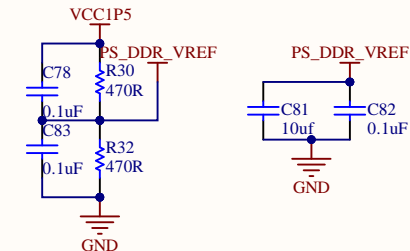
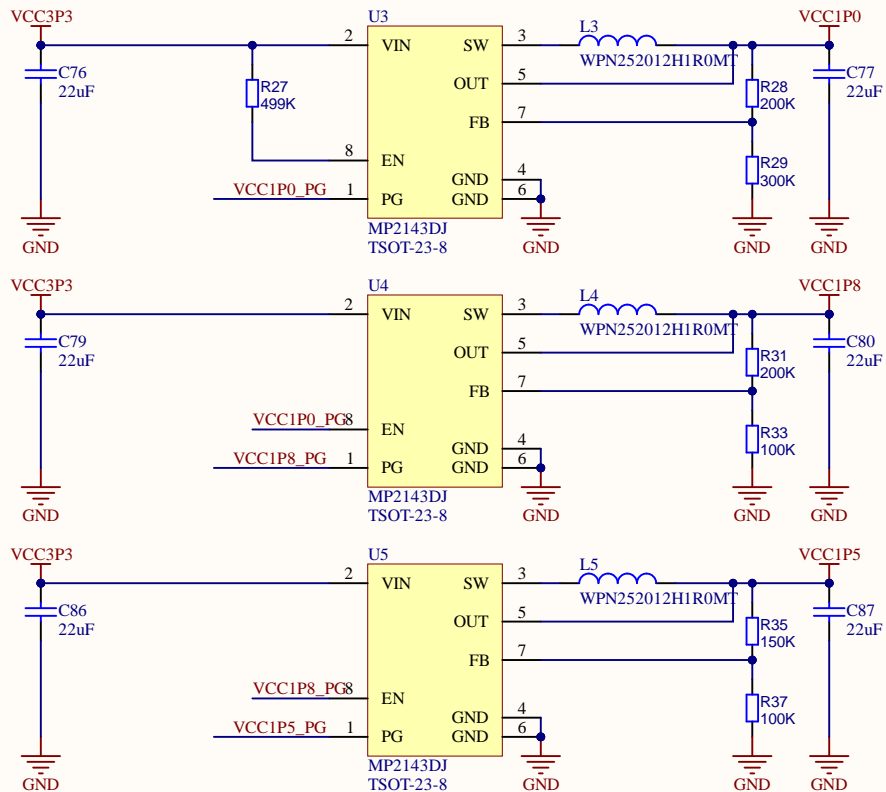
Title		
Size	Number	Revision
A4		
Date:	11-24-2023	Sheet of
File:	D:\AC608-7Z010\..PL_IO.SchDoc	Drawn By:

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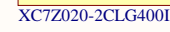
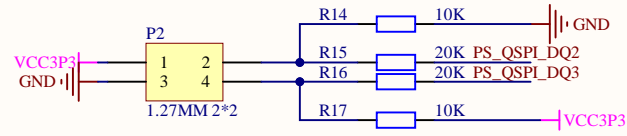
Title		
Size	Number	Revision
A4		
Date:	11-24-2023	Sheet of
File:	D:\AC608-7Z010\...\POWER.SchDoc	Drawn By:

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