

U1A

BANK 14

IO_0_14	P20	TP_INT	TP_INT
IO_L1P_T0_D00_MOSI_14	P22	QSPI_DQ0	QSPI_DQ0
IO_L1N_T0_D01_DIN_14	R22	QSPI_DQ1	QSPI_DQ1
IO_L2P_T0_D02_14	P21	QSPI_DQ2	QSPI_DQ2
IO_L2N_T0_D03_14	R21	QSPI_DQ3	QSPI_DQ3
IO_L3P_T0_DQS_PUDC_B_14	U22	E_GTXC	E_GTXC
IO_L3N_T0_DQS_EMCCLK_14	V22	E_RESET	E_RESET
IO_L4P_T0_D04_14	T21	TP_SCL	TP_SCL
IO_L4N_T0_D05_14	U21	E_TXD0	E_TXD0
IO_L5P_T0_D06_14	P19	E_RXD2	E_RXD2
IO_L5N_T0_D07_14	R19	E_RXD3	E_RXD3
IO_L6P_T0_FCS_B_14	T19	QSPI_CS	QSPI_CS
IO_L6N_T0_D08_VREF_14	T20	TP_SDA	TP_SDA
IO_L7P_T1_D09_14	W21	E_TXD2	E_TXD2
IO_L7N_T1_D10_14	W22	E_TXD1	E_TXD1
IO_L8P_T1_D11_14	AA20	AUD_DACDAT	AUD_DACDAT
IO_L8N_T1_D12_14	AA21	AUD_ADCDAT	AUD_ADCDAT
IO_L9P_T1_DQS_14	Y21	TP_RST	TP_RST
IO_L9N_T1_DQS_D13_14	Y22	E_TXD3	E_TXD3
IO_L10P_T1_D14_14	AB21	LCD_BL	LCD_BL
IO_L10N_T1_D15_14	AB22	LCD_DE	LCD_DE
IO_L11P_T1_SRCC_14	U20	AUD_BCLK	AUD_BCLK
IO_L11N_T1_SRCC_14	V20	AUD_XCLK	AUD_XCLK
IO_L12P_T1_MRCC_14	W19	E_RXC	E_RXC
IO_L12N_T1_MRCC_14	W20	AUD_LRCK	AUD_LRCK
IO_L13P_T2_MRCC_14	Y18	FPGA_GCLK1	FPGA_GCLK1
IO_L13N_T2_MRCC_14	Y19	LCD_VSYNC	LCD_VSYNC
IO_L14P_T2_SRCC_14	V18	E_RXD1	E_RXD1
IO_L14N_T2_SRCC_14	V19	LCD_HSYNC	LCD_HSYNC
IO_L15P_T2_DQS_RDWR_B_14	AA19	E_TXEN	E_TXEN
IO_L15N_T2_DQS_DOUT_CSO_B_14	AB20	LCD_PCLK	LCD_PCLK
IO_L16P_T2_CSI_B_14	V17	E_RXD0	E_RXD0
IO_L16N_T2_A15_D31_14	W17	E_MDIO	E_MDIO
IO_L17P_T2_A14_D30_14	AA18	E_MDC	E_MDC
IO_L17N_T2_A13_D29_14	AB18	E_RXDV	E_RXDV
IO_L18P_T2_A12_D28_14	U17	LCD_B4	LCD_B4
IO_L18N_T2_A11_D27_14	U18	LCD_B3	LCD_B3
IO_L19P_T3_A10_D26_14	P14	LCD_B2	LCD_B2
IO_L19N_T3_A09_D25_VREF_14	R14	LCD_B1	LCD_B1
IO_L20P_T3_A08_D24_14	R18	LCD_B0	LCD_B0
IO_L20N_T3_A07_D23_14	T18	LCD_G5	LCD_G5
IO_L21P_T3_DQS_14	N17	LCD_G4	LCD_G4
IO_L21N_T3_DQS_A06_D22_14	P17	LCD_G3	LCD_G3
IO_L22P_T3_A05_D21_14	P15	LCD_G2	LCD_G2
IO_L22N_T3_A04_D20_14	R16	LCD_G1	LCD_G1
IO_L23P_T3_A03_D19_14	N13	LCD_G0	LCD_G0
IO_L23N_T3_A02_D18_14	N14	LCD_R4	LCD_R4
IO_L24P_T3_A01_D17_14	P16	LCD_R3	LCD_R3
IO_L24N_T3_A00_D16_14	R17	LCD_R2	LCD_R2
IO_25_14	N15	LCD_R1	LCD_R1

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U1B

BANK 15

IO_0_15	J16	USB_SLCS	USB_SLCS
IO_L1P_T0_AD0P_15	H13	USB_FIFOADR	USB_FIFOADR1
IO_L1N_T0_AD0N_15	G13	USB_FD4	USB_FD4
IO_L2P_T0_AD8P_15	G15	USB_FD7	USB_FD7
IO_L2N_T0_AD8N_15	G16	USB_FD5	USB_FD5
IO_L3P_T0_DQS_AD1P_15	J14	USB_PKTEND	USB_PKTEND
IO_L3N_T0_DQS_AD1N_15	H14	USER_0	USER_0
IO_L4P_T0_15	G17	USB_FD1	USB_FD1
IO_L4N_T0_15	G18	USB_FD0	USB_FD0
IO_L5P_T0_AD9P_15	J15	USB_SLOE	USB_SLOE
IO_L5N_T0_AD9N_15	H15	USER_1	USER_1
IO_L6P_T0_15	H17	USB_FLAGA	USB_FLAGA
IO_L6N_T0_VREF_15	H18	USB_FLAGB	USB_FLAGB
IO_L7P_T1_AD2P_15	J22	LED5	LED5
IO_L7N_T1_AD2N_15	H22	LED6	LED6
IO_L8P_T1_AD10P_15	H20	USER_2	USER_2
IO_L8N_T1_AD10N_15	G20	USB_FD2	USB_FD2
IO_L9P_T1_DQS_AD3P_15	K21	LED3	LED3
IO_L9N_T1_DQS_AD3N_15	K22	LED4	LED4
IO_L10P_T1_AD11P_15	M21	LED7	LED7
IO_L10N_T1_AD11N_15	L21	LED2	LED2
IO_L11P_T1_SRCC_15	J20	USB_FD3	USB_FD3
IO_L11N_T1_SRCC_15	J21	FPGA_UART_RX	FPGA_UART_RX
IO_L12P_T1_MRCC_15	J19	USB_FD14	USB_FD14
IO_L12N_T1_MRCC_15	H19	USB_FD15	USB_FD15
IO_L13P_T2_MRCC_15	K18	USB_FD12	USB_FD12
IO_L13N_T2_MRCC_15	K19	USB_SLRD	USB_SLRD
IO_L14P_T2_SRCC_15	L19	USB_IFCLK	USB_IFCLK
IO_L14N_T2_SRCC_15	L20	USB_SLWR	USB_SLWR
IO_L15P_T2_DQS_15	N22	LED1	LED1
IO_L15N_T2_DQS_ADV_B_15	M22	LED0	LED0
IO_L16P_T2_A28_15	M18	SEG7_DIO	SEG7_DIO
IO_L16N_T2_A27_15	L18	CMOS_SDA	CMOS_SDA
IO_L17P_T2_A26_15	N18	HDMI2_HPD	HDMI2_HPD
IO_L17N_T2_A25_15	N19	BEEP	BEEP
IO_L18P_T2_A24_15	N20	CMOS_RST	CMOS_RST
IO_L18N_T2_A23_15	M20	CMOS_SCLK	CMOS_SCLK
IO_L19P_T3_A22_15	K13	USB_FIFOADR0	USB_FIFOADR0
IO_L19N_T3_A21_VREF_15	K14	USB_FD6	USB_FD6
IO_L20P_T3_A20_15	M13	IIC_SCL	IIC_SCL
IO_L20N_T3_A19_15	L13	USB_PA0	USB_PA0
IO_L21P_T3_DQS_15	K17	USB_FD13	USB_FD13
IO_L21N_T3_DQS_A18_15	J17	USB_FLAGC	USB_FLAGC
IO_L22P_T3_A17_15	L14	USB_FD8	USB_FD8
IO_L22N_T3_A16_15	L15	USB_FD9	USB_FD9
IO_L23P_T3_FOE_B_15	L16	USB_FD11	USB_FD11
IO_L23N_T3_FWE_B_15	K16	USB_FD10	USB_FD10
IO_L24P_T3_RS1_15	M15	FPGA_UART_TX	FPGA_UART_TX
IO_L24N_T3_RS0_15	M16	IIC_SDA	IIC_SDA
IO_25_15	M17	CMOS_XCLK	CMOS_XCLK

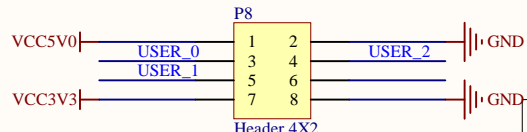
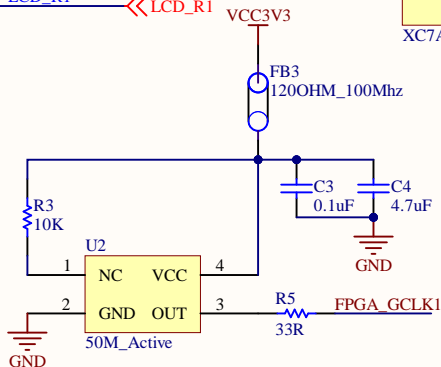
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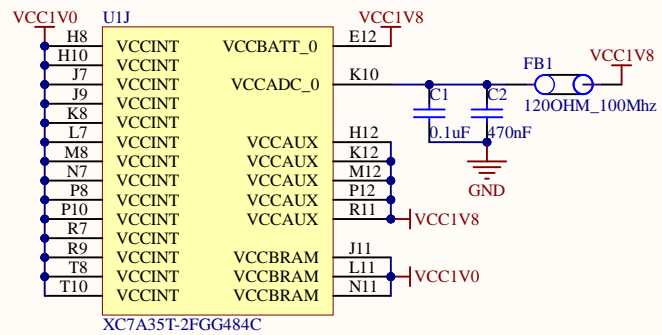
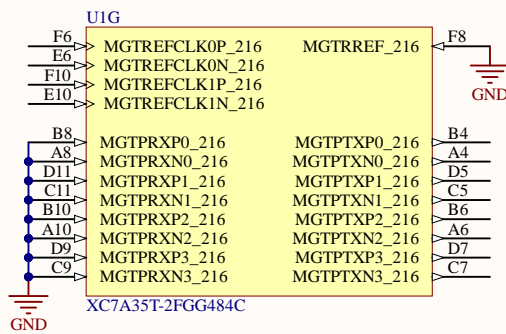
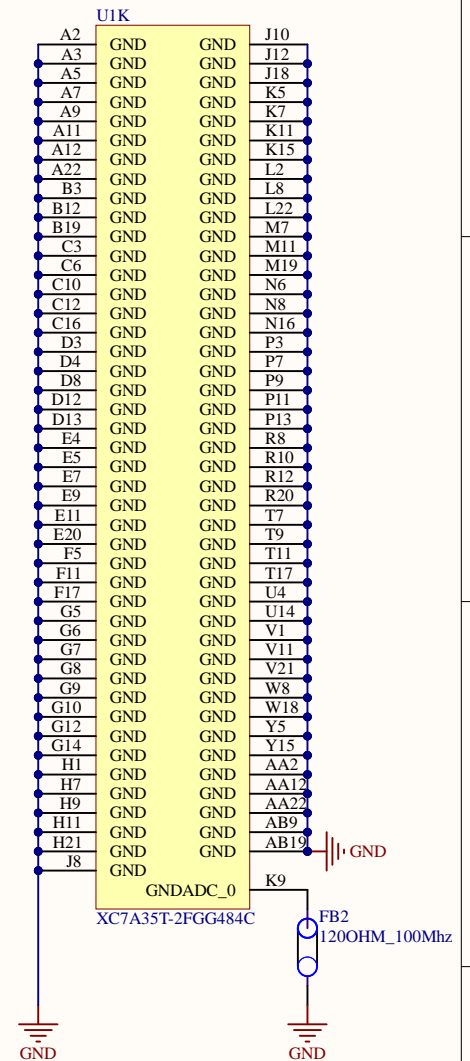
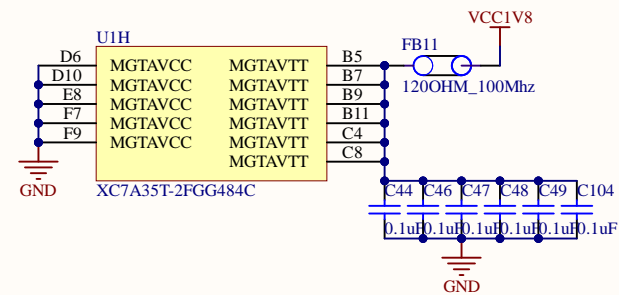
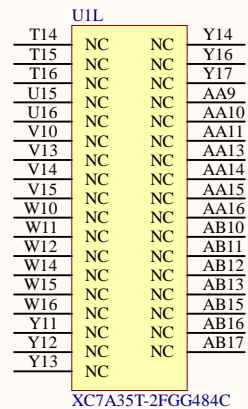
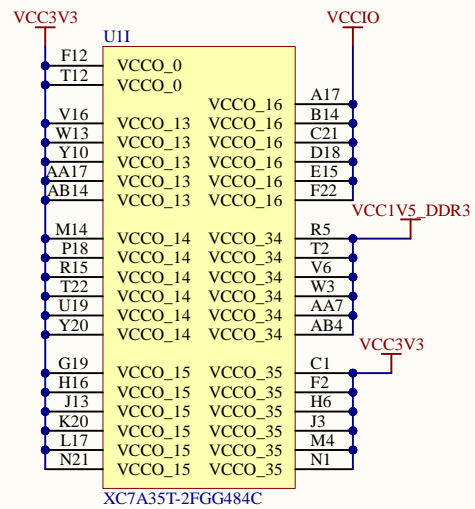
BANK 34

IO_0_34	T3	DDR3_CS0_N	DDR3_CS0_N
IO_L1P_T0_34	T1	DDR3_ADDR6	DDR3_ADDR6
IO_L1N_T0_34	U1	DDR3_ADDR4	DDR3_ADDR4
IO_L2P_T0_34	U2	DDR3_ADDR1	DDR3_ADDR1
IO_L2N_T0_34	V2	DDR3_ADDR0	DDR3_ADDR0
IO_L3P_T0_DQS_34	R3	DDR3_CK_P	DDR3_CK_P
IO_L3N_T0_DQS_34	R2	DDR3_CK_N	DDR3_CK_N
IO_L4P_T0_34	W2	DDR3_ADDR12	DDR3_ADDR12
IO_L4N_T0_34	Y2	DDR3_CKE0	DDR3_CKE0
IO_L5P_T0_34	W1	DDR3_BA2	DDR3_BA2
IO_L5N_T0_34	Y1	DDR3_BA1	DDR3_BA1
IO_L6P_T0_34	U3	DDR3_ADDR8	DDR3_ADDR8
IO_L6N_T0_VREF_34	V3	DDR3_ADDR2	DDR3_ADDR2
IO_L7P_T1_34	AA1	DDR3_DQ15	DDR3_DQ15
IO_L7N_T1_34	AB1	DDR3_DQ14	DDR3_DQ14
IO_L8P_T1_34	AB3	DDR3_DM1	DDR3_DM1
IO_L8N_T1_34	AB2	DDR3_DQ10	DDR3_DQ10
IO_L9P_T1_DQS_34	Y3	DDR3_DQS_P1	DDR3_DQS_P1
IO_L9N_T1_DQS_34	AA3	DDR3_DQS_N1	DDR3_DQS_N1
IO_L10P_T1_34	AA5	DDR3_DQ9	DDR3_DQ9
IO_L10N_T1_34	AB5	DDR3_DQ8	DDR3_DQ8
IO_L11P_T1_SRCC_34	Y4	DDR3_DQ13	DDR3_DQ13
IO_L11N_T1_SRCC_34	AA4	DDR3_DQ12	DDR3_DQ12
IO_L12P_T1_MRCC_34	V4	DDR3_DQ11	DDR3_DQ11
IO_L12N_T1_MRCC_34	W4		
IO_L13P_T2_MRCC_34	R4	DDR3_ADDR11	DDR3_ADDR11
IO_L13N_T2_MRCC_34	T4	DDR3_ADDR3	DDR3_ADDR3
IO_L14P_T2_SRCC_34	T5	DDR3_ADDR5	DDR3_ADDR5
IO_L14N_T2_SRCC_34	U5	DDR3_BA0	DDR3_BA0
IO_L15P_T2_DQS_34	W6	DDR3_CAS_N	DDR3_CAS_N
IO_L15N_T2_DQS_34	W5	DDR3_WE_N	DDR3_WE_N
IO_L16P_T2_34	U6	DDR3_ADDR9	DDR3_ADDR9
IO_L16N_T2_34	V5	DDR3_ODT0	DDR3_ODT0
IO_L17P_T2_34	R6	DDR3_ADDR7	DDR3_ADDR7
IO_L17N_T2_34	T6	DDR3_ADDR13	DDR3_ADDR13
IO_L18P_T2_34	Y6	DDR3_RAS_N	DDR3_RAS_N
IO_L18N_T2_34	AA6	DDR3_ADDR10	DDR3_ADDR10
IO_L19P_T3_34	V7	DDR3_DQ7	DDR3_DQ7
IO_L19N_T3_VREF_34	W7	DDR3_DQ4	DDR3_DQ4
IO_L20P_T3_34	AB7	DDR3_DQ0	DDR3_DQ0
IO_L20N_T3_34	AB6	DDR3_DQ3	DDR3_DQ3
IO_L21P_T3_DQS_34	V9	DDR3_DQS_P0	DDR3_DQS_P0
IO_L21N_T3_DQS_34	V8	DDR3_DQS_N0	DDR3_DQS_N0
IO_L22P_T3_34	AA8		
IO_L22N_T3_34	Y8	DDR3_DQ1	DDR3_DQ1
IO_L23P_T3_34	Y7	DDR3_DQ2	DDR3_DQ2
IO_L23N_T3_34	W9	DDR3_DM0	DDR3_DM0
IO_L24P_T3_34	Y9	DDR3_DQ5	DDR3_DQ5
IO_L24N_T3_34	U7	DDR3_RESET_N	DDR3_RESET_N
IO_25_34			

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Title		
Size	Number	Revision
A4		
Date:	9-29-2024	Sheet of
File:	C:\Users\...\01_7A35T_FPGA_IO.SchDoc	Drawn By:



Title		
Size A4	Number	Revision
Date:	9-29-2024	Sheet of
File:	C:\Users\...\03_7A35T_FPGA_PowerBank_SchDocBy:	

A

B

C

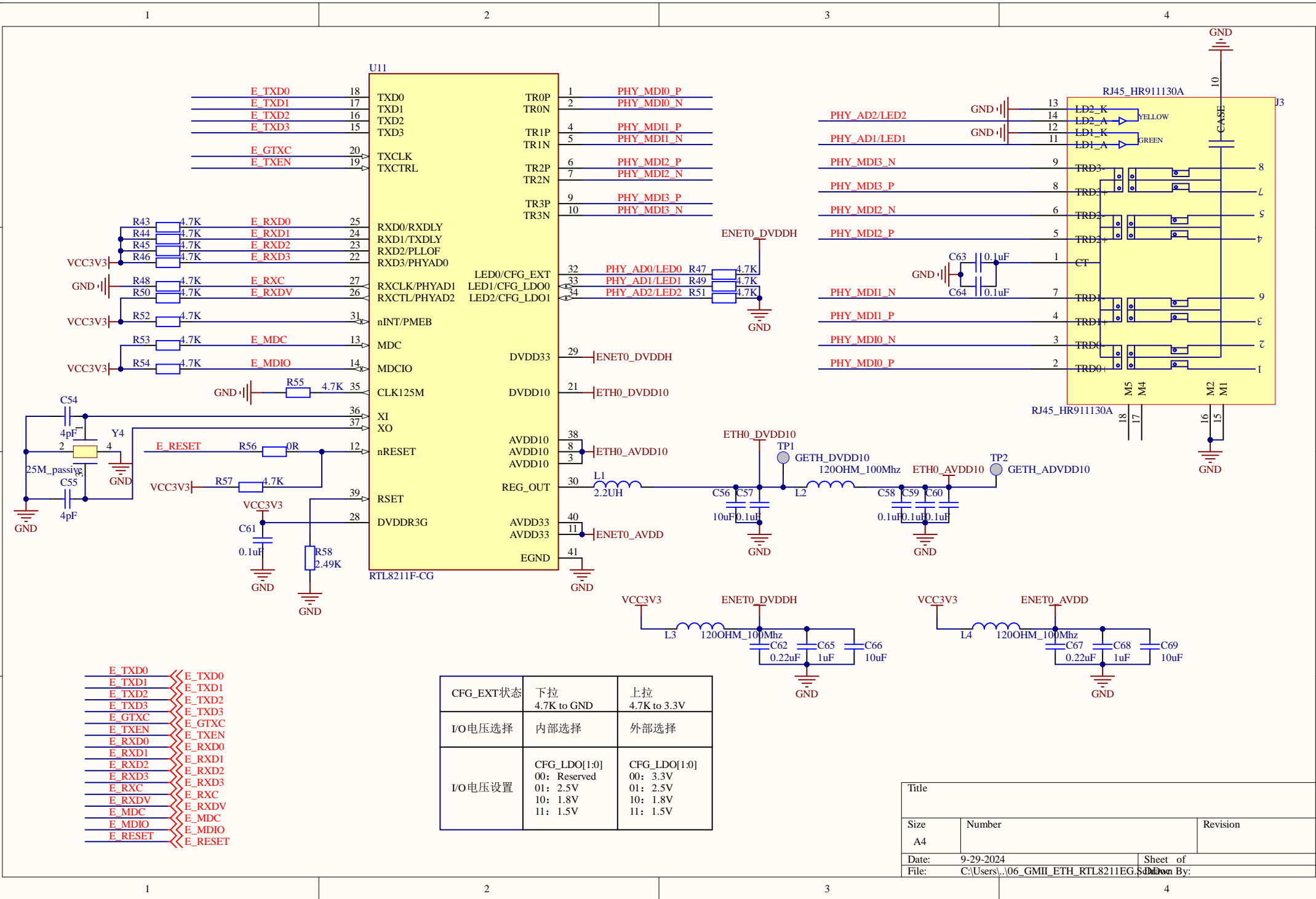
D

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1

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3

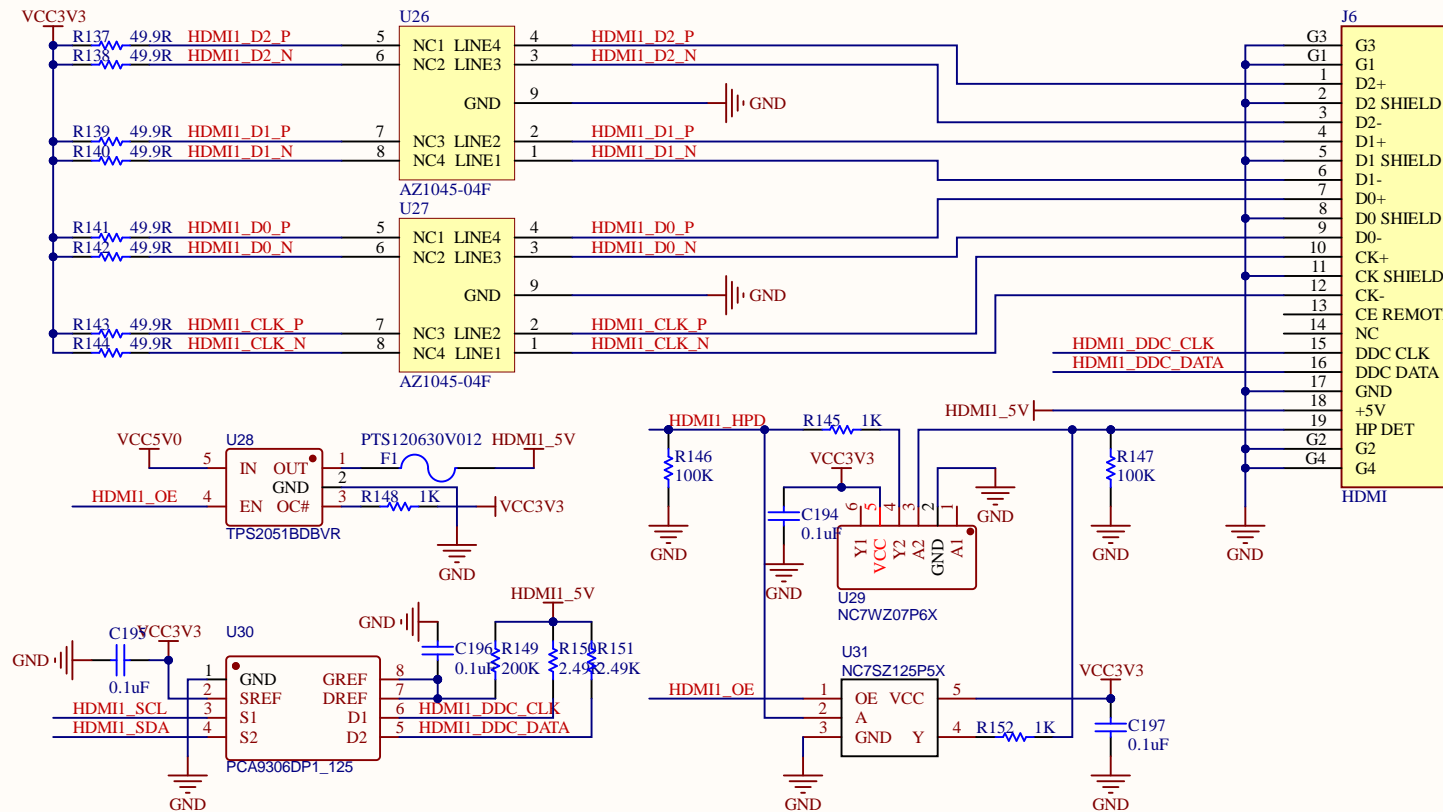
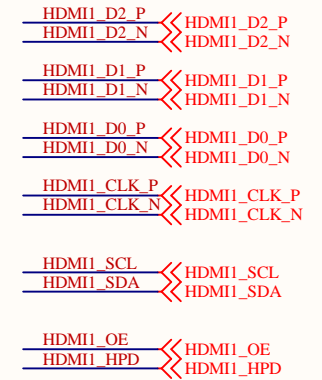
4

1

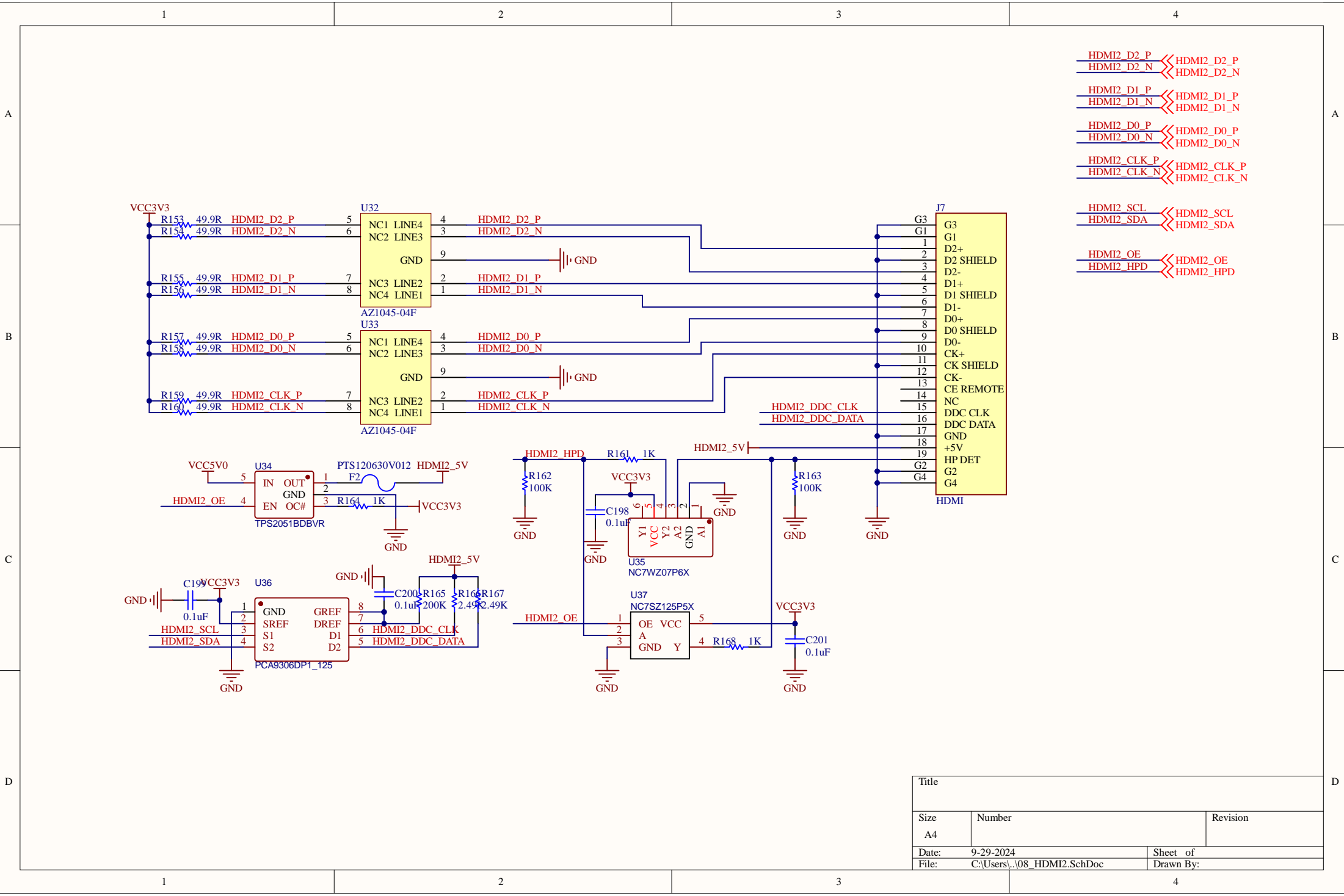
2

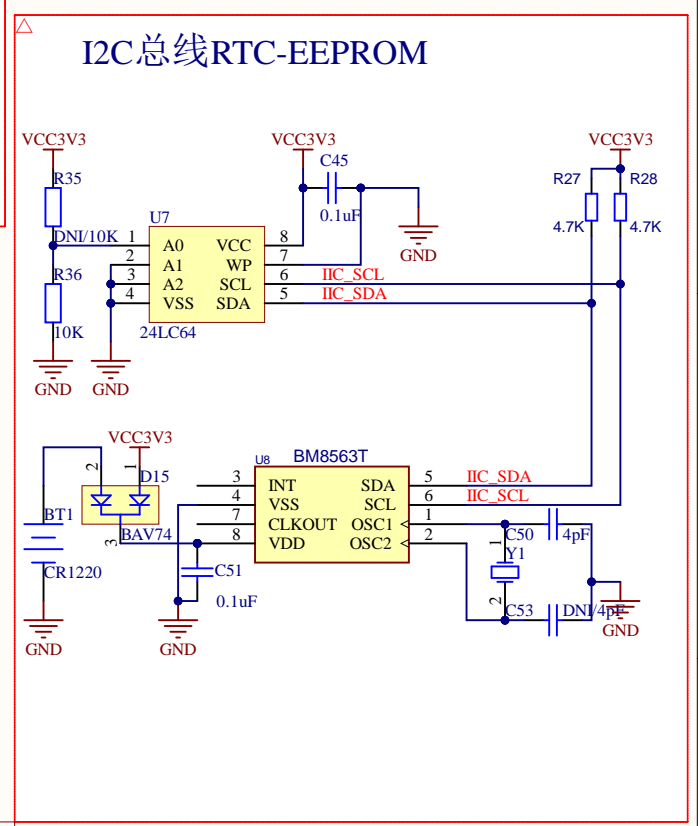
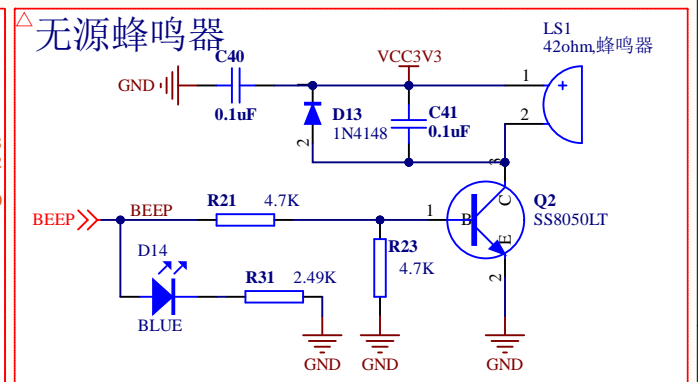
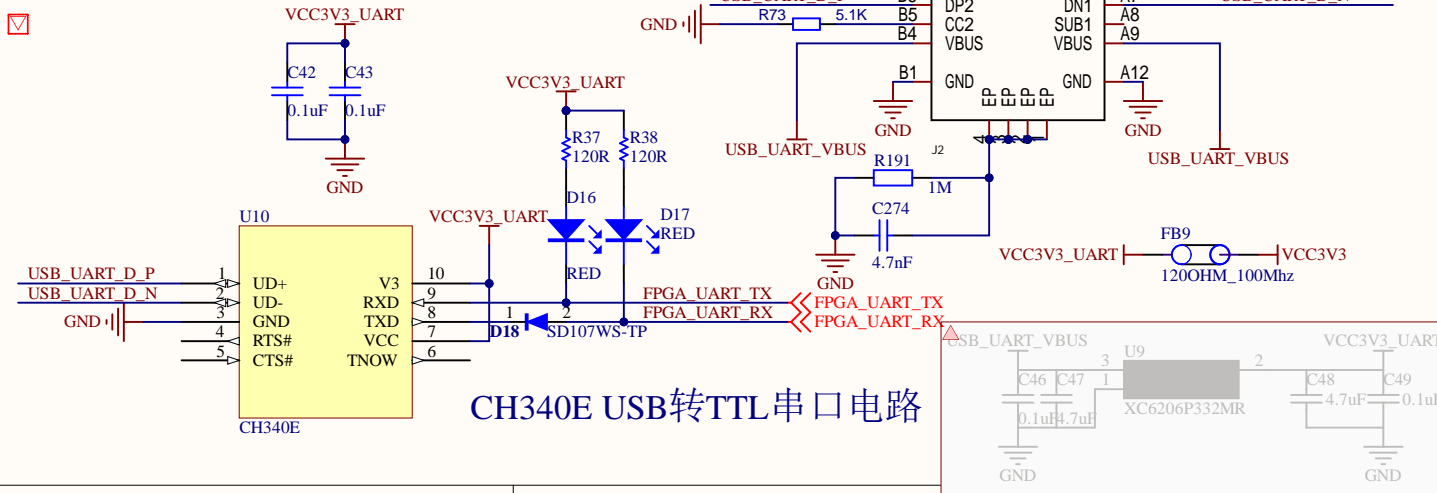
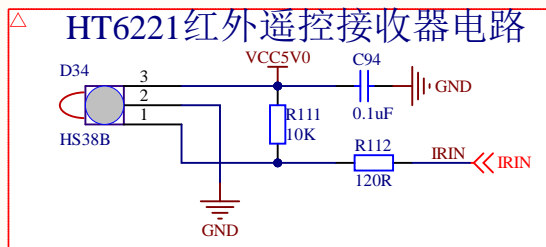
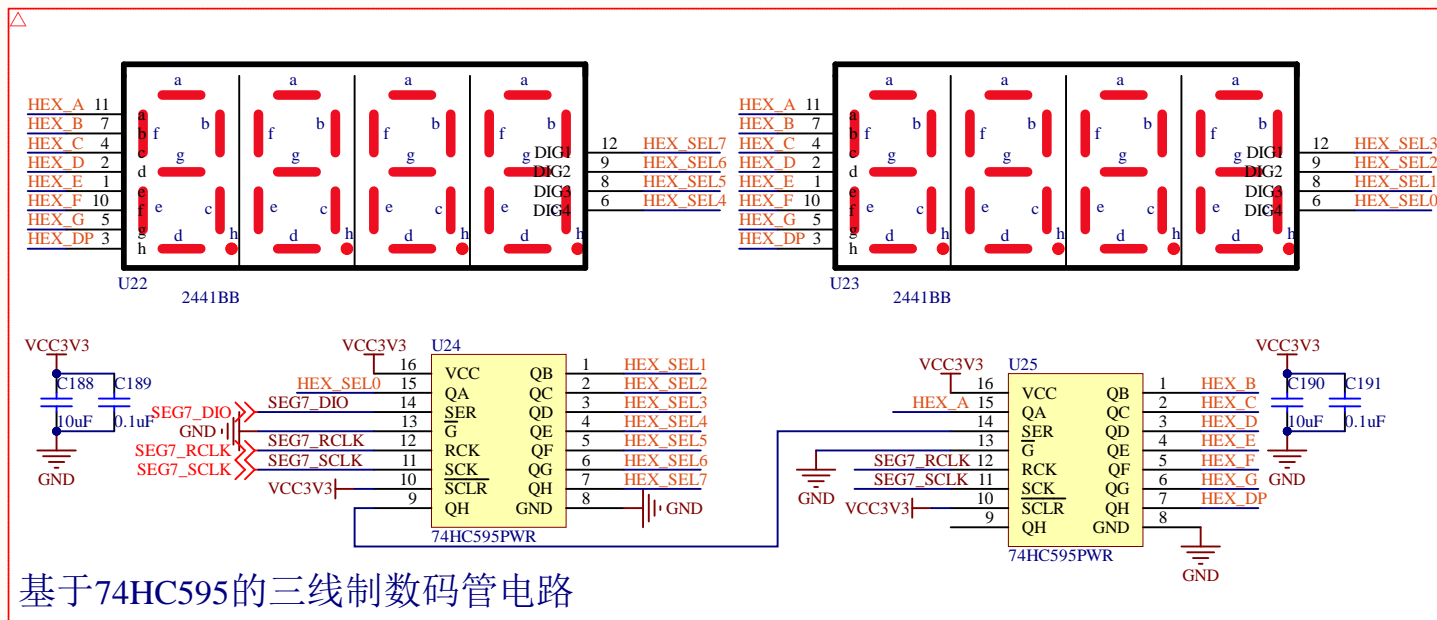
3

4

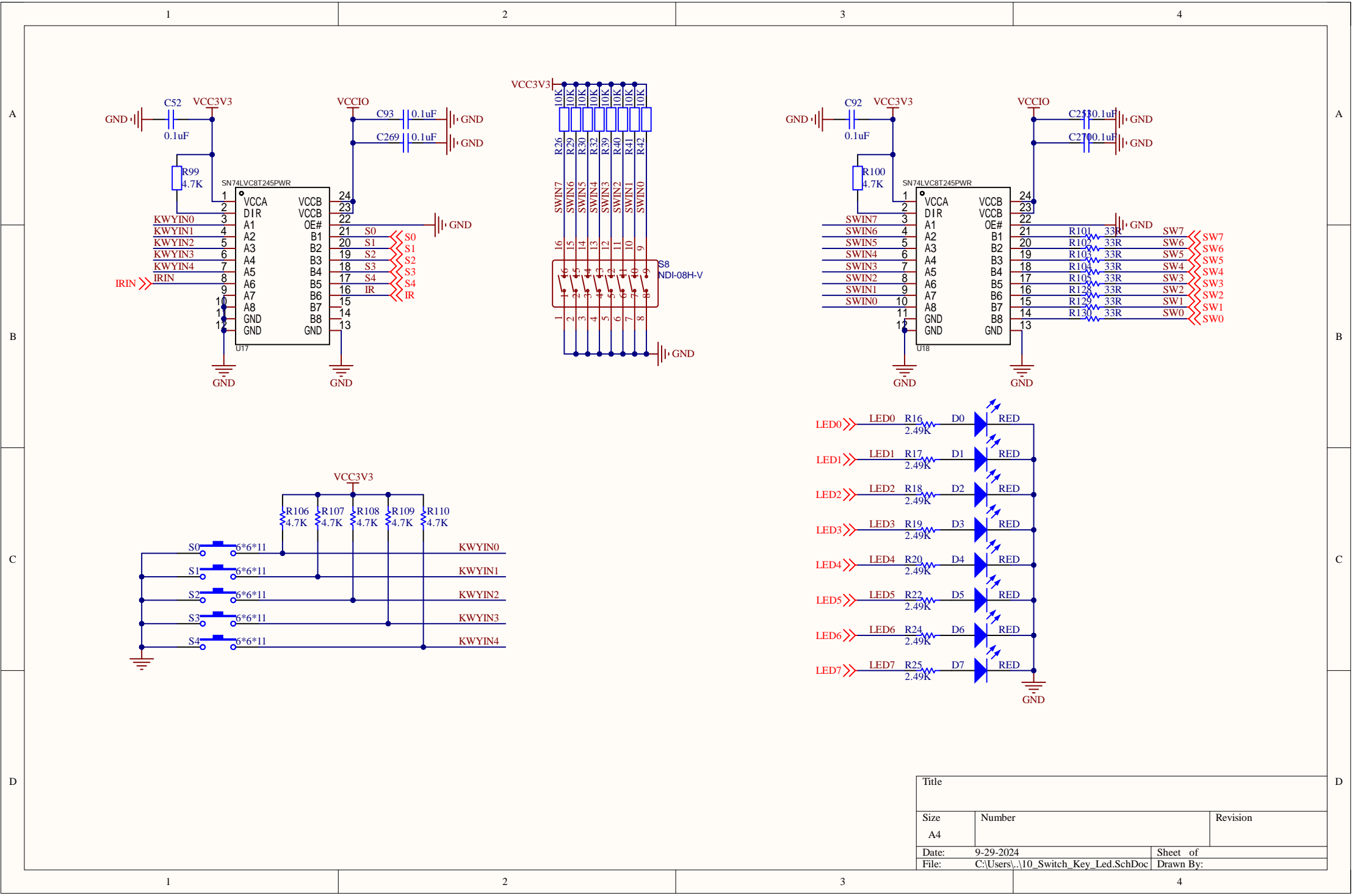


Title		
Size A4	Number	Revision
Date:	9-29-2024	Sheet of
File:	C:\Users\...\07_HDMI1.SchDoc	Drawn By:

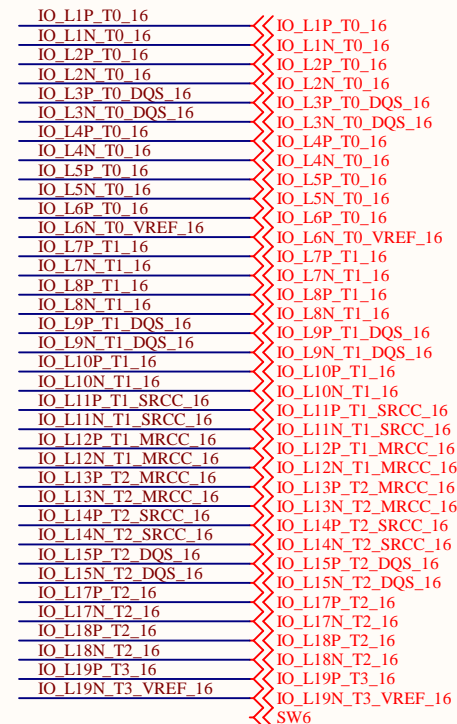
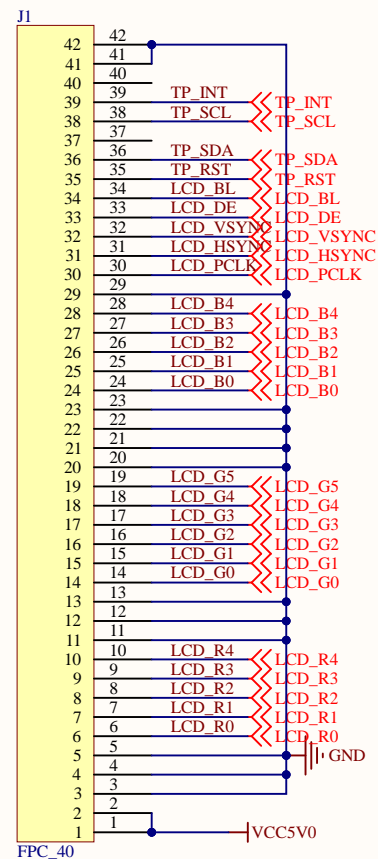
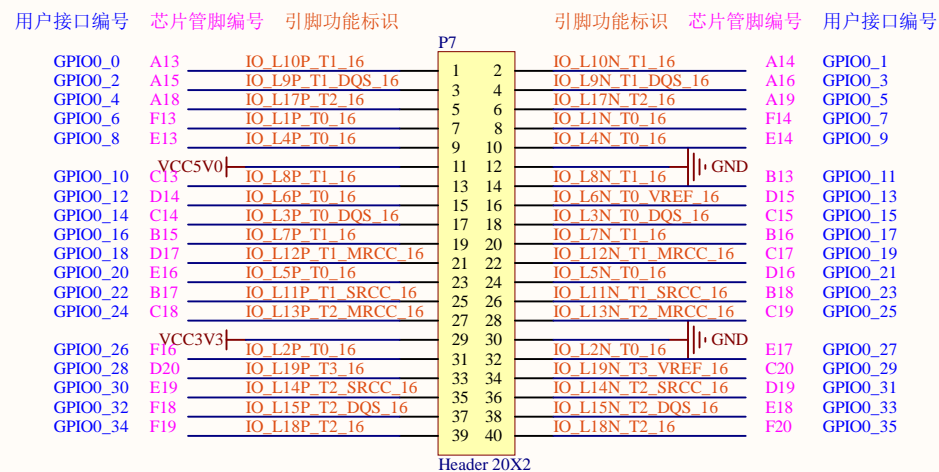
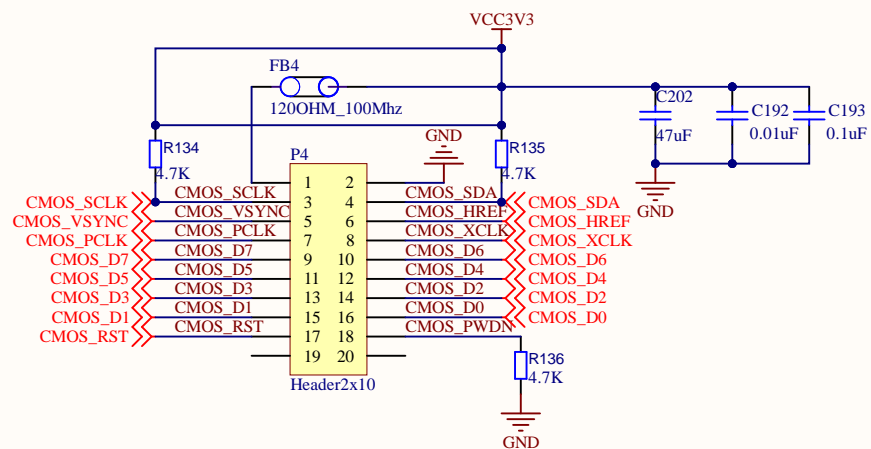




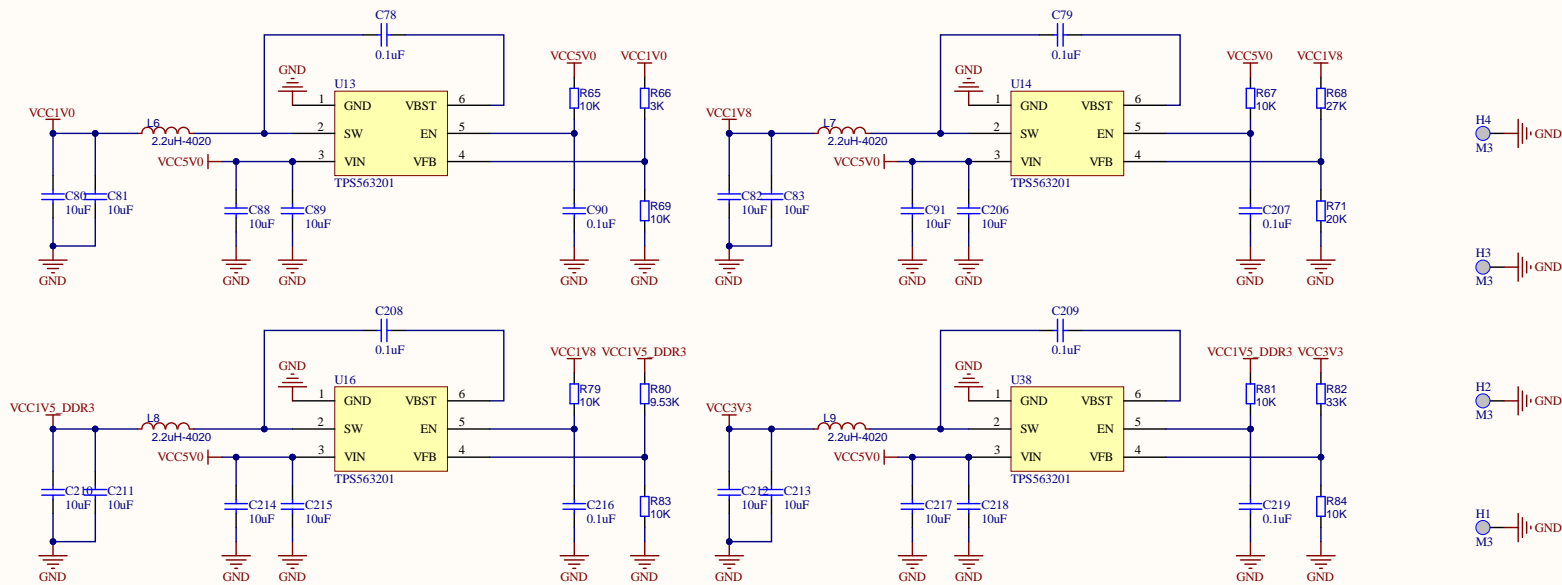
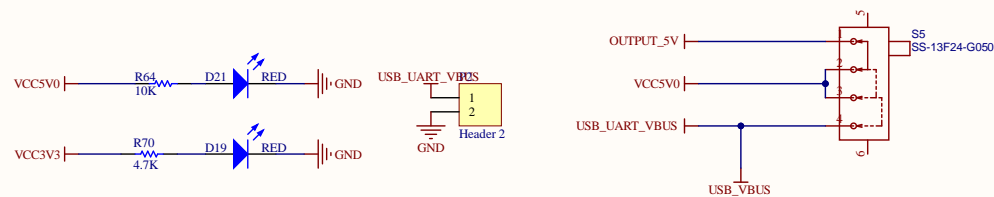
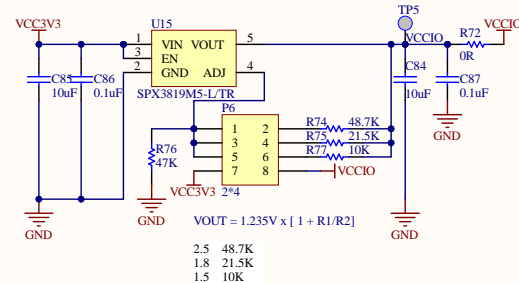
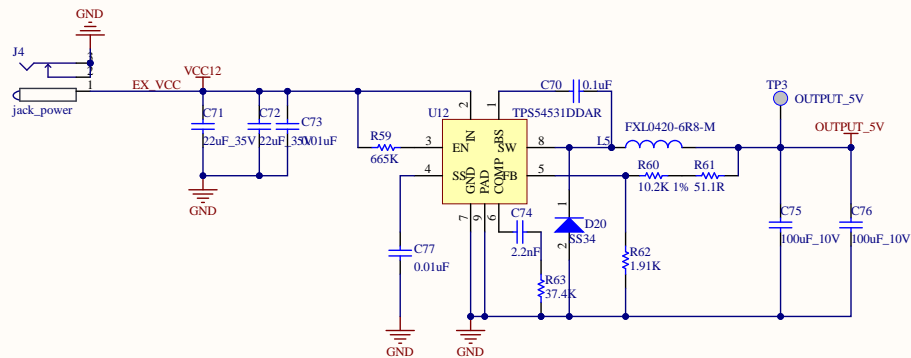
Title		
Size	Number	Revision
A4		
Date:	9-29-2024	Sheet of
File:	C:\Users\...\09_UART_I2C_BEEP_IR_HEX_Sch.Dwg	1 of 1



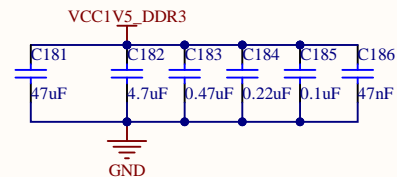
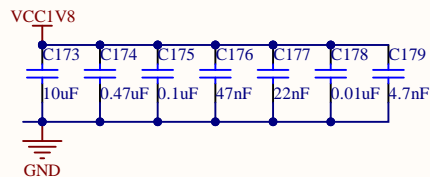
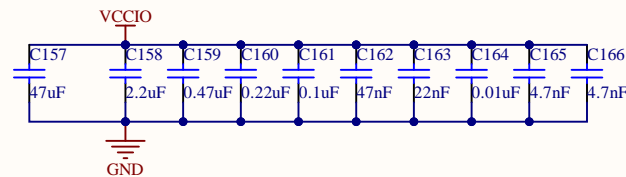
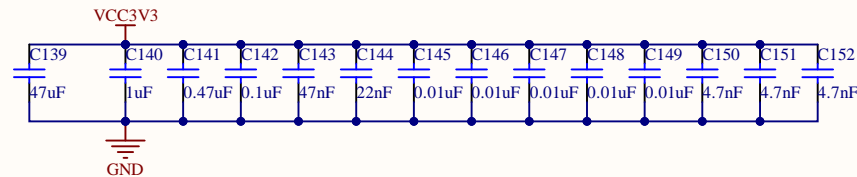
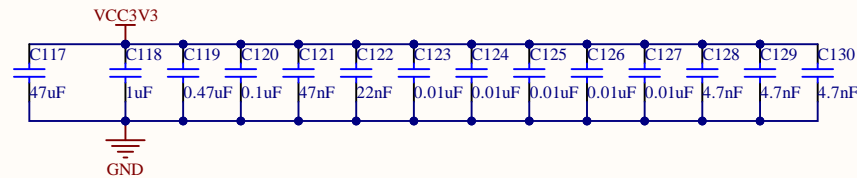
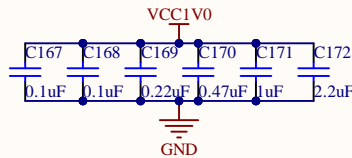
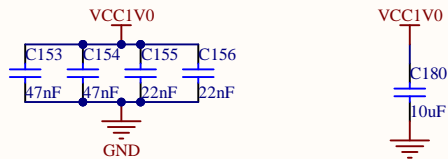
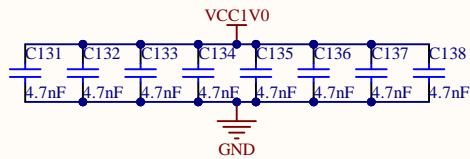
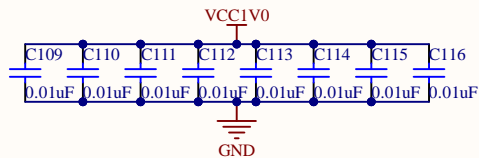
Title		
Size	Number	Revision
A4		
Date:	9-29-2024	Sheet of
File:	C:\Users\...\10_Switch_Key_Led.SchDoc	Drawn By:



Title		
Size A4	Number	Revision
Date: 9-29-2024	File: C:\Users\...\11_GPIO0_GPIO2_Camera_Sch	Sheet of Drawn By:

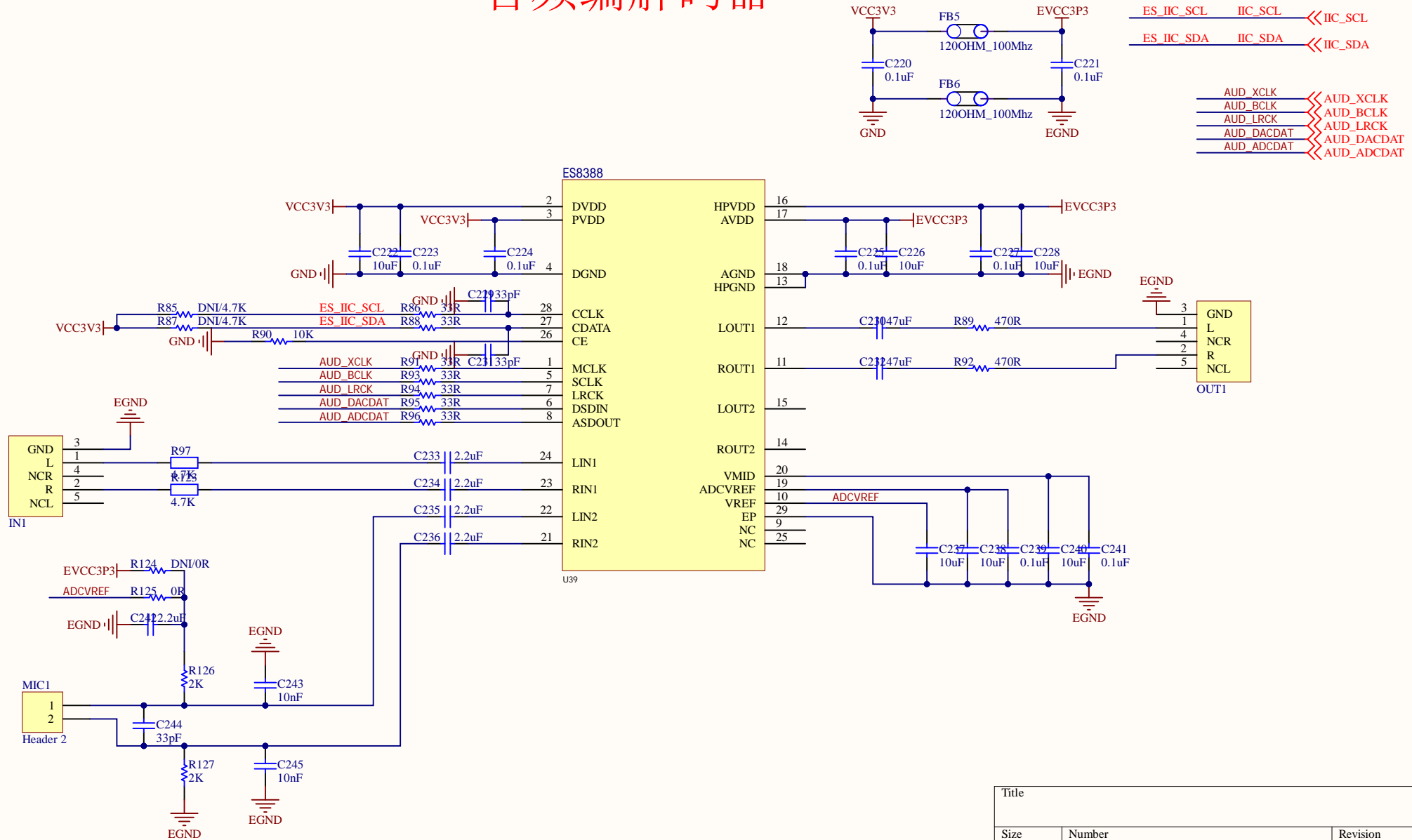


Title		
Size A3	Number	Revision
Date: 9-29-2024	Sheet of	
File: C:\Users\...\12_BoardPower.SchDoc	Drawn By:	



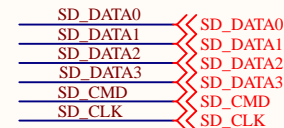
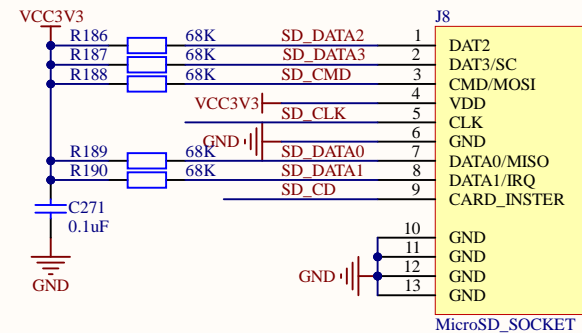
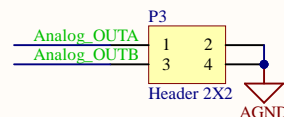
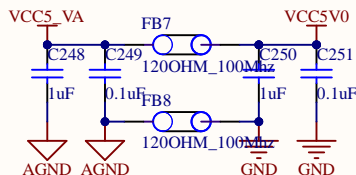
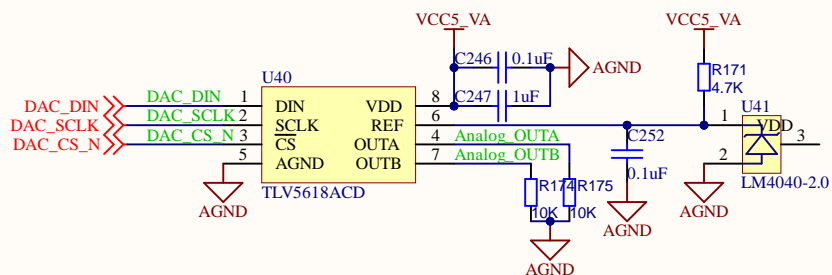
Title		
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File:	C:\Users\...\13_Decoupling.SchDoc	Drawn By:

音频编解码器

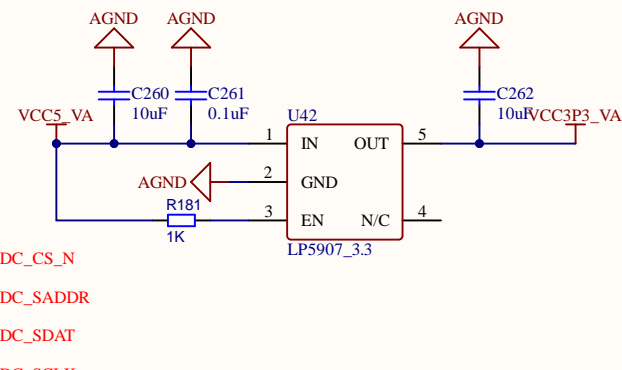
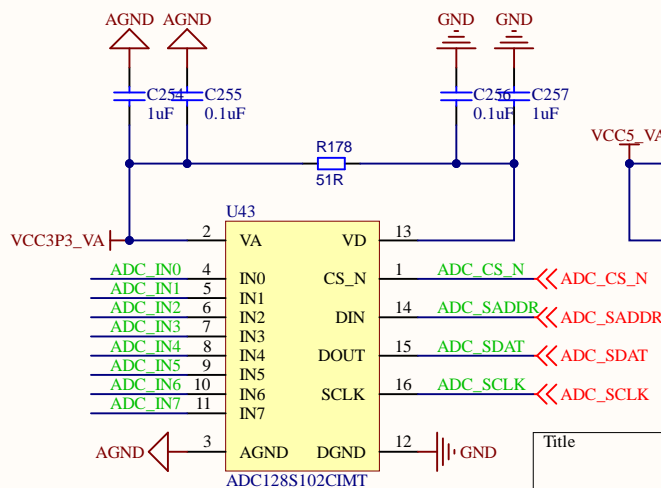
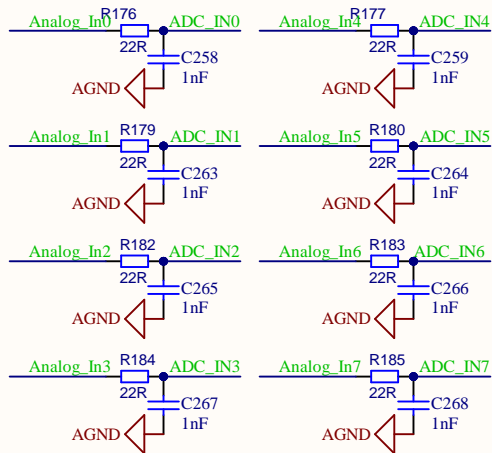
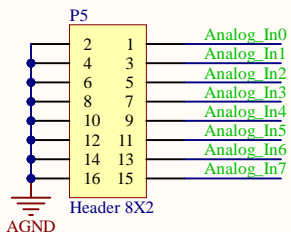


Title		
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File:	C:\Users\...\14_Audio.SchDoc	Drawn By:

TLV5618 2通道12位DAC电路



ADC128S022 8通道12位ADC电路



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File:	C:\Users\...\15_ADDA.SchDoc	Drawn By: