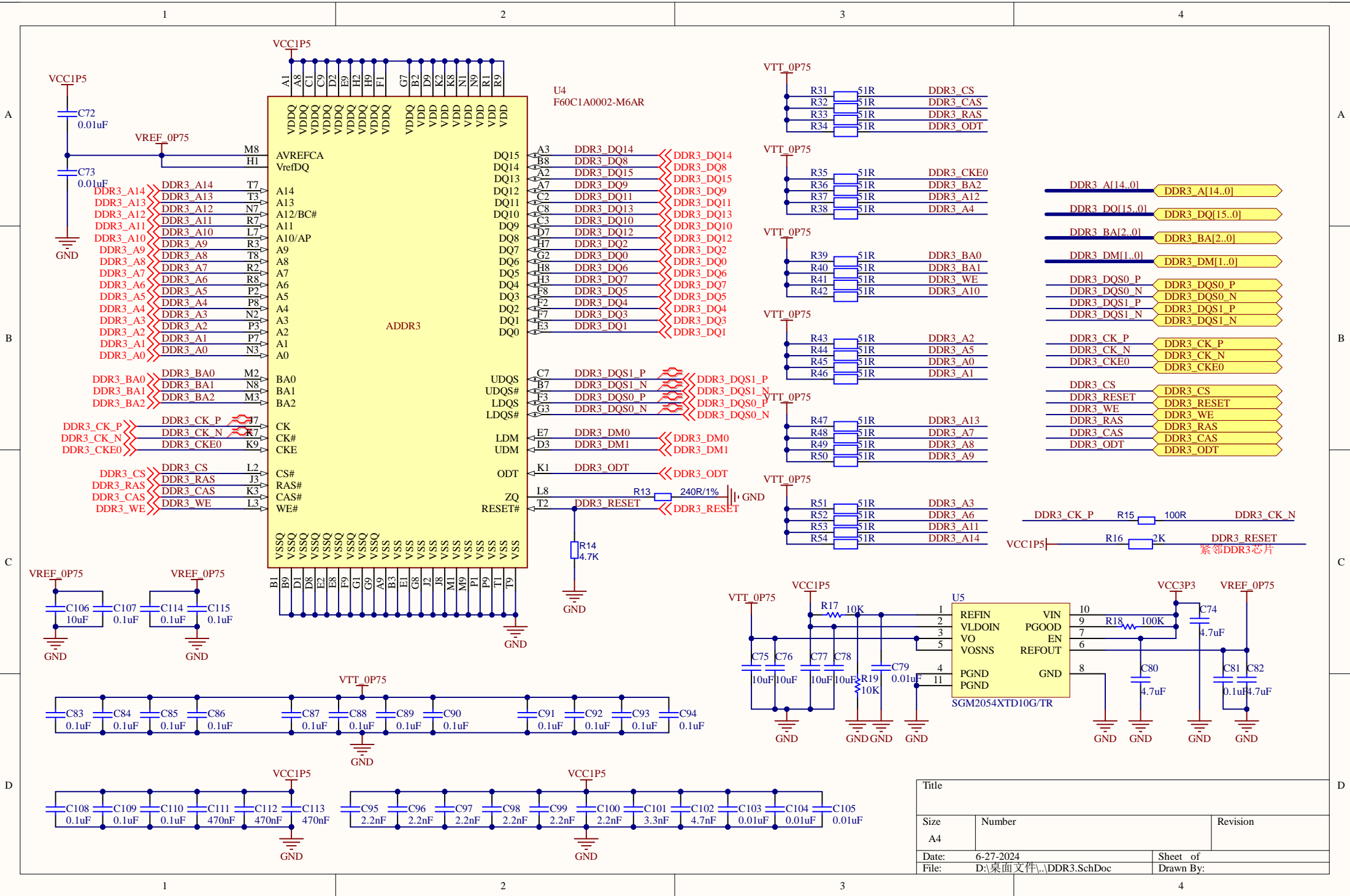


Title		
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Size A4	Number	Revision
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Bank0			
DDR3_A8	D4	IOT29A/LVDS/PUDC_B	IOT54B/LVDS
DDR3_A6	C4	IOT29B/LVDS	IOT54A/LVDS
DDR3_A13	B2	IOT31A/LVDS	IOT52B/LVDS
DDR3_A9	A2	IOT31B/LVDS	IOT52A/LVDS
DDR3_BA0	D6	IOT33A/LVDS	IOT50B/LVDS
DDR3_A7	C6	IOT33B/LVDS	IOT50A/LVDS
DDR3_A2	B3	IOT35A/LVDS	IOT48B/LVDS
DDR3_A0	A3	IOT35B/LVDS	IOT48A/LVDS
DDR3_A4	B4	IOT37A/LVDS	IOT45B/LVDS
DDR3_BA1	A4	IOT37B/LVDS	IOT45A/LVDS
DDR3_CKE0	C5	IOT39A/LVDS	IOT43B/LVDS
DDR3_A12	A5	IOT39B/LVDS	IOT43A/LVDS
DDR3_ODT	F7	IOT41A/LVDS	IOT41B/LVDS
DDR3_CK_P	D9	IOT56A/LVDS/GCLKT_0/TPLL_T_IN1	
DDR3_CK_N	C9	IOT56B/LVDS/GCLKC_0/TPLL_C_FB1	
B0_T58_P	B9	IOT58A/LVDS/GCLKT_1/TPLL_T_IN2	
B0_T58_N	A9	IOT58B/LVDS/GCLKC_1/TPLL_C_FB0	

U1A

A8	B0_T54_N
B8	
F8	DDR3_CAS
G8	DDR3_RAS
C8	DDR3_A5
D8	DDR3_BA2
A7	DDR3_RESET
C7	DDR3_A3
E8	DDR3_WE
E7	DDR3_CS
A6	DDR3_A1
B6	DDR3_A11
E6	DDR3_A10

VCC\_EXT

DDR3_DQS0_P	D11
DDR3_DQS0_N	C11
DDR3_DQ5	C10
DDR3_DQ7	A10
DDR3_DQ2	G9
DDR3_DM0	F9
DDR3_DQ1	B11
DDR3_DQ4	A11
DDR3_DQ3	G11
DDR3_DQ6	F10
	B12
DDR3_DQ0	A12
DDR3_DQ11	F11
DDR3_DQ10	E11
DDR3_DQ13	D12
DDR3_DQ12	C12
DDR3_DQ8	C13
DDR3_DQ9	A13

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Bank1			
IOT61A/LVDS/DQS0/DQ0/GCLKT_2/TPLL_T_IN0			
IOT61B/LVDS/DQS0/DQ0/GCLKC_2			
IOT63A/LVDS/DQ0/GCLKT_3/RPLL0_T_IN0			
IOT63B/LVDS/DQ0/GCLKC_3			
IOT66A/LVDS/DQ0			
IOT66B/LVDS/DQ0			
IOT68A/LVDS/DQ0			
IOT68B/LVDS/DQ0			
IOT70A/LVDS/DQ0			
IOT70B/LVDS/DQ0			
IOT72A/LVDS/DQ0			
IOT72B/LVDS/DQ0			
IOT74A/LVDS/DQ1/DQS_01			
IOT74B/LVDS/DQ1/DQS_01			
IOT76A/LVDS/DQ1			
IOT76B/LVDS/DQ1			
IOT78A/LVDS/DQ1			
IOT78B/LVDS/DQ1			
IOT91B/LVDS/RPLL0_C_FB0			
IOT91A/LVDS/RPLL0_T_IN1			
IOT89B/LVDS			
IOT89A/LVDS/RPLL0_T_FB1			
IOT87B/LVDS			
IOT87A/LVDS			
IOT85B/LVDS/DQ1			
IOT85A/LVDS/DQ1			
IOT83B/LVDS/DQ1			
IOT83A/LVDS/DQ1			
IOT80B/LVDS/DQS1/DQ1			
IOT80A/LVDS/DQS1/DQ1			

U1B

Bank3			
IOB65A/LVDS	P15		
IOB65B/LVDS/S_DOUT	P16		
IOB67A/LVDS	L14		
IOB67B/LVDS	M13		
IOB69A/LVDS/DQ5	M14		
IOB69B/LVDS/DQ5	N14		
IOB71A/LVDS/DQ5	U17		
IOB71B/LVDS/DQ5	T17		
IOB73A/LVDS/DQ5	T18		
IOB73B/LVDS/DQ5			
IOB75A/LVDS/DQS5/DQ5/GCLKT_6B	N15		
IOB75B/LVDS/DQS5/DQ5/GCLKC_6B	N16		
IOB77A/LVDS/DQ5	P17		
IOB77B/LVDS/DQ5	P18		
IOB79A/LVDS/DQ5/DQS_45	P17		
IOB79B/LVDS/DQ5/DQS_45	N18		
IOB81A/LVDS/DQ4	M16		
IOB81B/LVDS/DQ4	M18		
IOB83A/LVDS/DQ4	L17		
IOB83B/LVDS/DQ4	L18		
IOB85A/LVDS/DQS4/DQ4	K17		
IOB85B/LVDS/DQS4/DQ4	K18		
IOB87A/LVDS/DQ4	J16		
IOB87B/LVDS/DQ4	J18		
IOB89A/LVDS/DQ4/GCLKT_7/BPLL_T_IN0	H17		
IOB89B/LVDS/DQ4/GCLKC_7	H18		
IOB91A/LVDS/DQ4/GCLKT_6A	L15		
IOB91B/LVDS/DQ4/GCLKC_6A	L16		

U1D

Bank2			
IOR5A/LVDS		IOR33B/LVDS/GCLKC_5/RPLL1_C_FB0	
IOR5B/LVDS		IOR33A/LVDS/GCLKT_5/RPLL1_T_IN1	
IOR7A/LVDS/DQ2		IOR31B/LVDS/DQ3/GCLKC_4/RPLL1_C_FB1	
IOR7B/LVDS/DQ2		IOR31A/LVDS/DQ3/GCLKT_4/RPLL1A_T_IN0	
IOR9A/LVDS/DQ2		IOR29B/LVDS/DQ3	
IOR9B/LVDS/DQ2/MCKTEST		IOR29A/LVDS/DQ3	
IOR12A/LVDS/DQS2/DQ2		IOR26B/LVDS/DQS3/DQ3	
IOR12B/LVDS/DQS2/DQ2/ADCINCLK		IOR26A/LVDS/DQS3/DQ3	
IOR14A/LVDS/DQ2		IOR24B/LVDS/DQ3	
IOR14B/LVDS/DQ2/ADCOTEST		IOR24A/LVDS/DQ3/RPLL1B_T_IN0	
IOR16A/LVDS/DQ2		IOR22B/LVDS/DQ3	
IOR16B/LVDS/DQ2		IOR22A/LVDS/DQ3	
IOR18A/LVDS/DQ2/DQS_23		IOR20B/LVDS/DQ3	
IOR18B/LVDS/DQ2/DQS_23		IOR20A/LVDS/DQ3	

U1C

K16	B2_R33_N
K15	B2_R33_P
L12	B2_R31_N
L13	B2_R31_P
K13	B2_R29_N
K12	B2_R29_P
K14	B2_R26_N
J13	B2_R26_P
H16	
H15	B2_R24_P
G18	B2_R22_N
G16	B2_R22_P
F18	B2_R20_N
F17	B2_R20_P

VCC\_EXT

VCC\_EXT

DDR3_A[14..0]	DDR3_A[14..0]
DDR3_DQ[15..0]	DDR3_DQ[15..0]
DDR3_BA[2..0]	DDR3_BA[2..0]
DDR3_DM[1..0]	DDR3_DM[1..0]
DDR3_DQS0_P	DDR3_DQS0_P
DDR3_DQS0_N	DDR3_DQS0_N
DDR3_DQS1_P	DDR3_DQS1_P
DDR3_DQS1_N	DDR3_DQS1_N
DDR3_CK_P	DDR3_CK_P
DDR3_CK_N	DDR3_CK_N
DDR3_CKE0	DDR3_CKE0
DDR3_CS	DDR3_CS
DDR3_RESET	DDR3_RESET
DDR3_WE	DDR3_WE
DDR3_RAS	DDR3_RAS
DDR3_CAS	DDR3_CAS
DDR3_ODT	DDR3_ODT

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Bank4

Bank5

CLK\_G T9  
B4\_B29\_N V9  
B4\_B31\_P R8  
B4\_B31\_N T8  
B4\_B33\_P U10  
B4\_B33\_N V10  
B4\_B35\_P R10  
B4\_B35\_N T10  
READY U3  
FLASH\_CS V3  
B4\_B39\_P U11  
B4\_B39\_N V11  
B4\_B41\_P N10  
B4\_B41\_N P11  
B4\_B43\_P T12  
B4\_B43\_N V12  
B4\_B45\_P R11  
B4\_B45\_N T11

UIE



V17 DONE  
T15 MODE0  
R15 FLASH\_CLK  
V16 B4\_B60\_N  
U16 B4\_B60\_P  
T13 FLASH\_MOSI  
R13 FLASH\_MISO  
V15 B4\_B56\_N  
U15 B4\_B56\_P  
V14 FLASH\_HOLD  
T14 FLASH\_WP  
P12 B4\_B52\_N  
N12 MODE1  
V13 B4\_B50\_N  
U13 B4\_B50\_P  
N11 B4\_B48\_N  
M11 B4\_B48\_P

MO\_VDD

RECONFIG V2  
B5\_B2\_P T4  
V4  
B5\_B4\_P N5  
B5\_B4\_N P6  
B5\_B6\_P R7  
B5\_B6\_N T7  
B5\_B8\_P R3  
B5\_B8\_N T3  
B5\_B10\_P U5  
B5\_B10\_N V5  
B5\_B12\_P R5  
B5\_B12\_N T5  
B5\_B14\_P N6  
B5\_B14\_N P7  
B5\_B16\_P T6  
B5\_B16\_N V6  
N7  
B5\_B18\_N P8  
B5\_B20\_P U7  
B5\_B20\_N V7  
B5\_B22\_P U8  
B5\_B22\_N V8  
B5\_B24\_P M8  
B5\_B24\_N N8  
B5\_B26\_P M10  
B5\_B26\_N N9

VCC\_EXT

IOB1A/S\_RECONFIG  
IOB2A/LVDS  
IOB2B/LVDS  
IOB4A/LVDS/S\_D08/SDA/LPLL1\_T\_FB0  
IOB4B/LVDS/D09/SCL  
IOB6A/LVDS  
IOB6B/LVDS  
IOB8A/LVDS/S\_D05/SSPI\_SO  
IOB8B/LVDS/S\_D06  
IOB10A/LVDS/S\_D03/SSPI\_CSN  
IOB10B/LVDS/S\_D04/SSPI\_SI  
IOB12A/LVDS/GCLKT\_10B/S\_D07/SSPI\_WPN  
IOB12B/LVDS/GCLKC\_10B/S\_RDWR\_B/LPLL1\_C\_IN1  
IOB14A/LVDS/SSPI\_CLK  
IOB14B/LVDS/SSPI\_HOLDN  
IOB16A/LVDS  
IOB16B/LVDS  
IOB18A/LVDS  
IOB18B/LVDS  
IOB20A/LVDS  
IOB20B/LVDS  
IOB22A/LVDS  
IOB22B/LVDS  
IOB24A/LVDS  
IOB24B/LVDS  
IOB26A/LVDS/GCLKT\_12  
IOB26B/LVDS/GCLKC\_12

UIF

GW5A-LV25UG324C2M1

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Bank6

Bank7

IOL7A/LVDS/DQ7  
IOL7B/LVDS/DQ7  
IOL9A/LVDS/DQS7/DQ7  
IOL9B/LVDS/DQS7/DQ7  
IOL12A/LVDS/DQ7  
IOL12B/LVDS/DQ7  
IOL14A/LVDS/DQ7/LPLL1A\_T\_IN0  
IOL14B/LVDS/DQ7  
IOL16A/LVDS/DQ6/DQS\_67  
IOL16B/LVDS/DQ6/DQS\_67  
IOL18A/LVDS/DQ6  
IOL18B/LVDS/DQ6  
IOL31B/LVDS  
IOL31A/LVDS  
IOL29B/LVDS  
IOL29A/LVDS  
IOL27B/LVDS/DQ6  
IOL27A/LVDS/DQ6  
IOL25B/LVDS/DQ6  
IOL25A/LVDS/DQ6  
IOL23B/LVDS/DQ6  
IOL23A/LVDS/DQ6  
IOL21B/LVDS/DQS6/DQ6  
IOL21A/LVDS/DQS6/DQ6

UIG

Bank6

Bank6

Bank6

Bank6

Bank6

Bank6

Bank6

Bank6

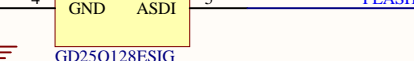
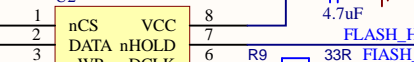
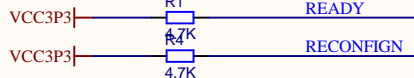
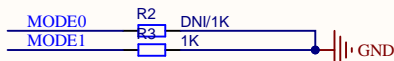
Bank6

Bank6

Bank6

Bank6

P3 B6\_L31\_N MIPI\_RX\_D3\_N  
P4 B6\_L31\_P MIPI\_RX\_D3\_P  
N3 B6\_L29\_N MIPI\_RX\_D2\_N  
N4 B6\_L29\_P MIPI\_RX\_D2\_P  
M5 B6\_L27\_N MIPI\_RX\_CLK\_N  
L6 B6\_L27\_P MIPI\_RX\_CLK\_P  
U1 B6\_L25\_N MIPI\_RX\_D1\_N  
U2 B6\_L25\_P MIPI\_RX\_D1\_P  
T1 B6\_L23\_N MIPI\_RX\_D0\_N  
T2 B6\_L23\_P MIPI\_RX\_D0\_P  
P1 B6\_L21\_N  
P2 B6\_L21\_P



VCC\_EXT

B7\_T1\_P H4  
B7\_T1\_N H3  
B7\_T3\_P L5  
B7\_T3\_N K5  
H6  
B7\_T5\_N H5  
B7\_T7\_P G3  
B7\_T7\_N G1  
B7\_T9\_P H7  
B7\_T9\_N G6  
B7\_T11\_P F2  
B7\_T11\_N F1  
B7\_T13\_P J7  
B7\_T13\_N J6  
B7\_T15\_P E3  
B7\_T15\_N E1

UIH

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

Bank7

C1 B7\_T27\_N  
C2 B7\_T27\_P  
F5 B7\_T25\_N  
F6 B7\_T25\_P  
D3 B7\_T23\_N  
E4 B7\_T23\_P  
K6 B7\_T21\_N  
L7 B7\_T21\_P  
D1 B7\_T19\_N  
D2 B7\_T19\_P  
F3 B7\_T17\_N  
F4 B7\_T17\_P

UII

Bank7

Bank7

Bank7

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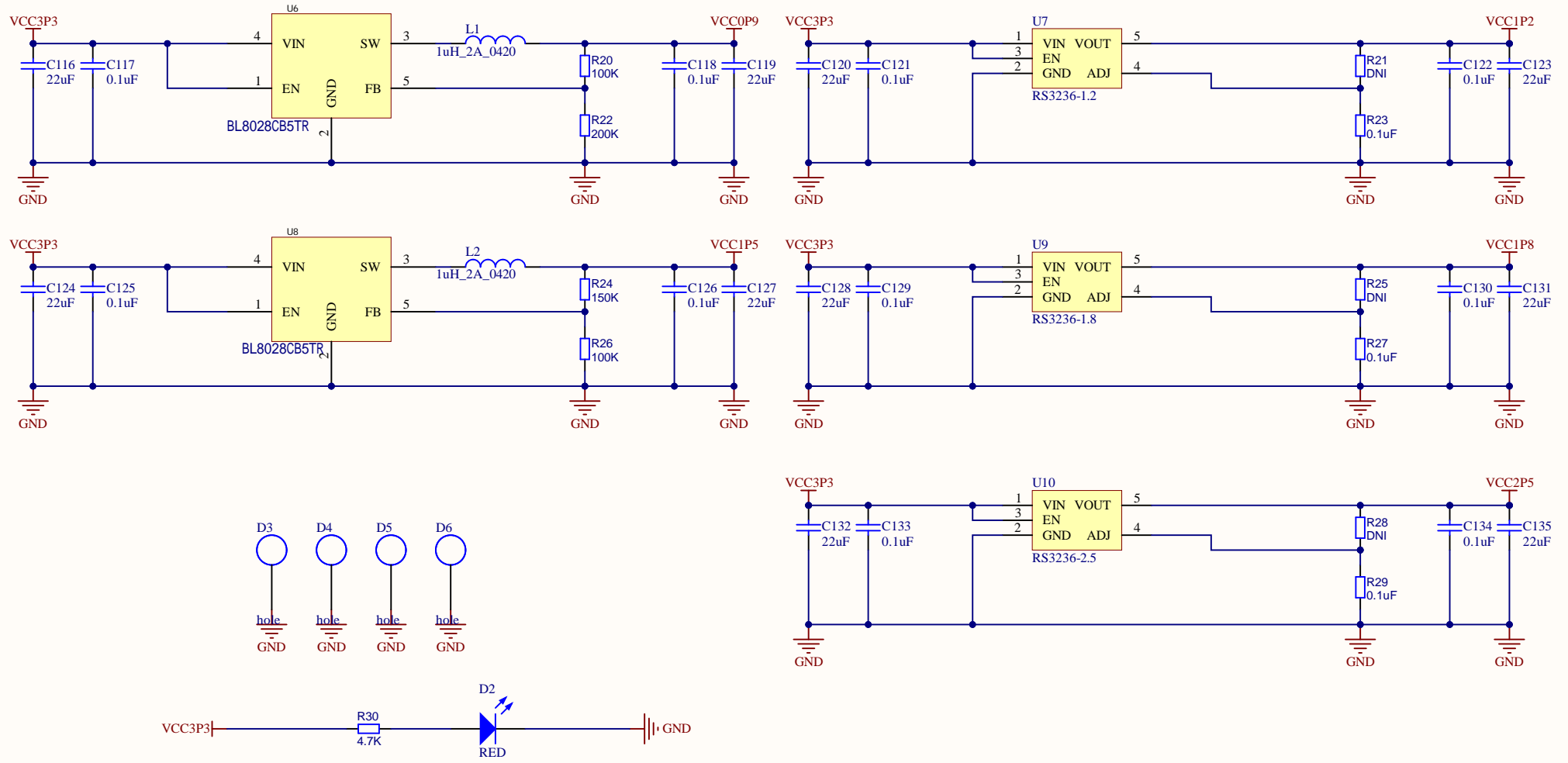
Bank7

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Bank7



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