

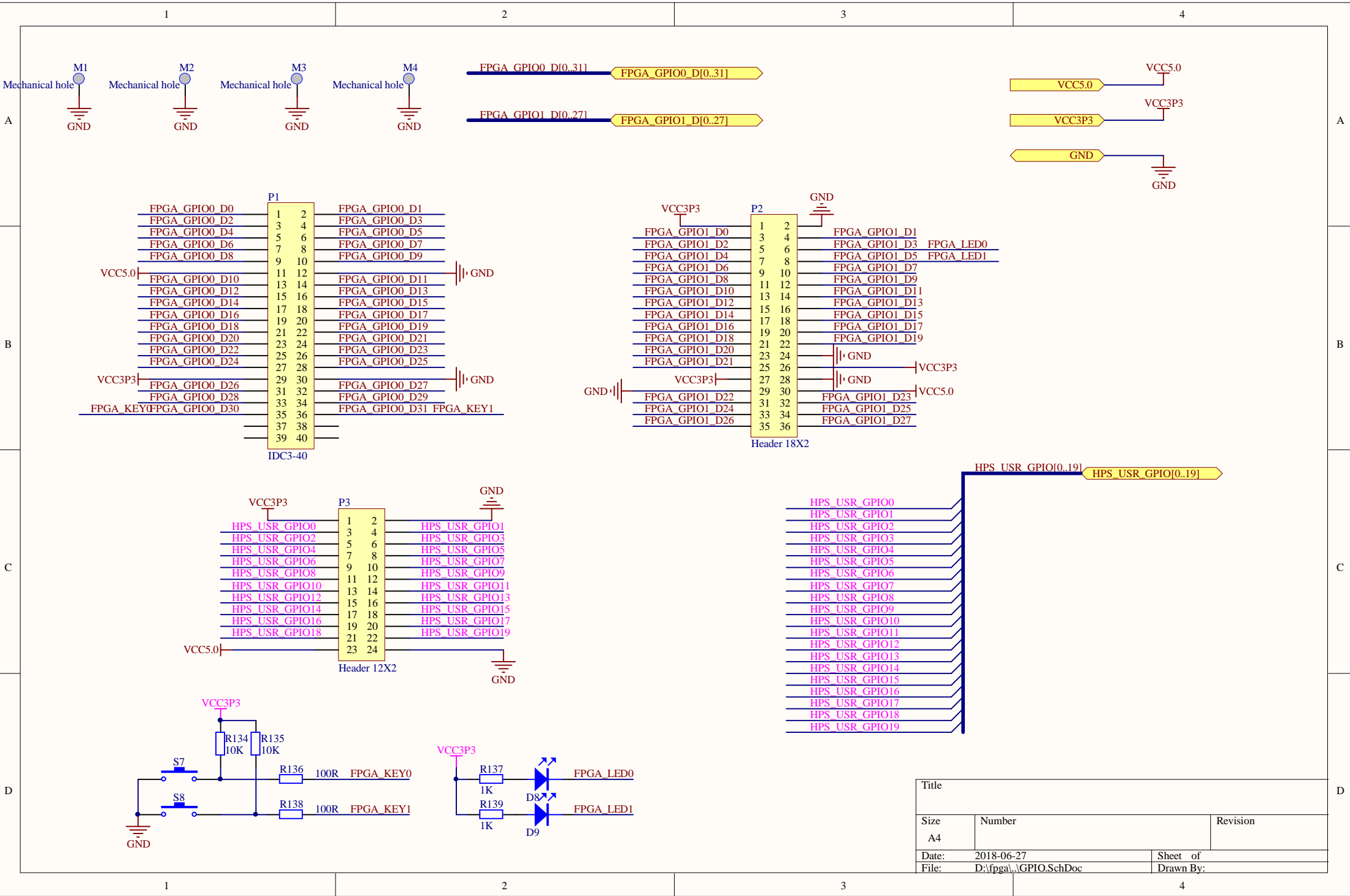






D





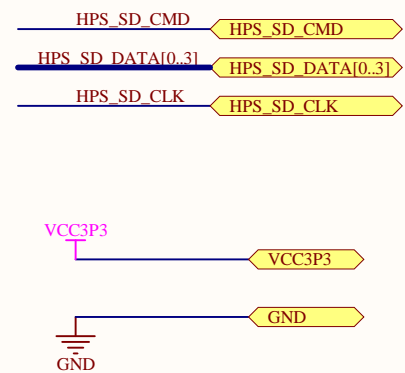
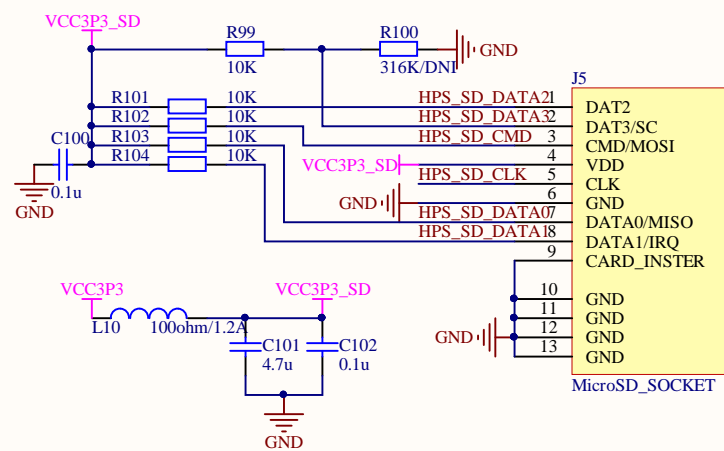
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Date:	2018-06-27	Sheet of
File:	D:\fpga\GPIO.SchDoc	Drawn By:







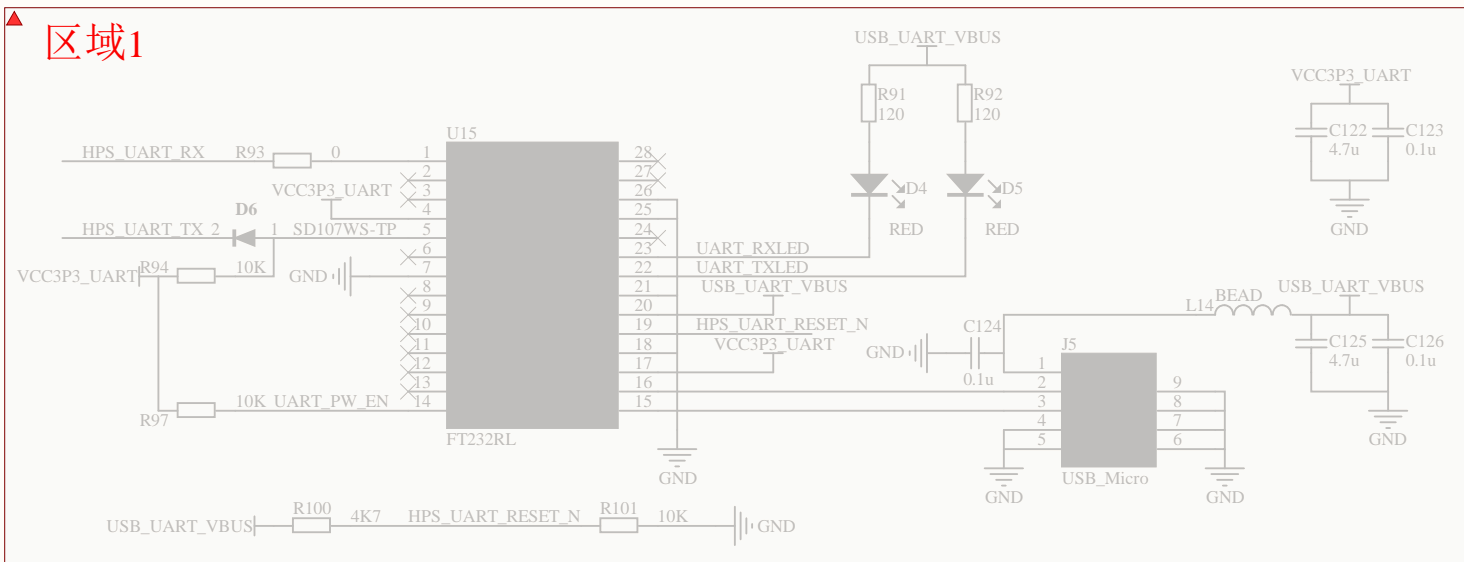




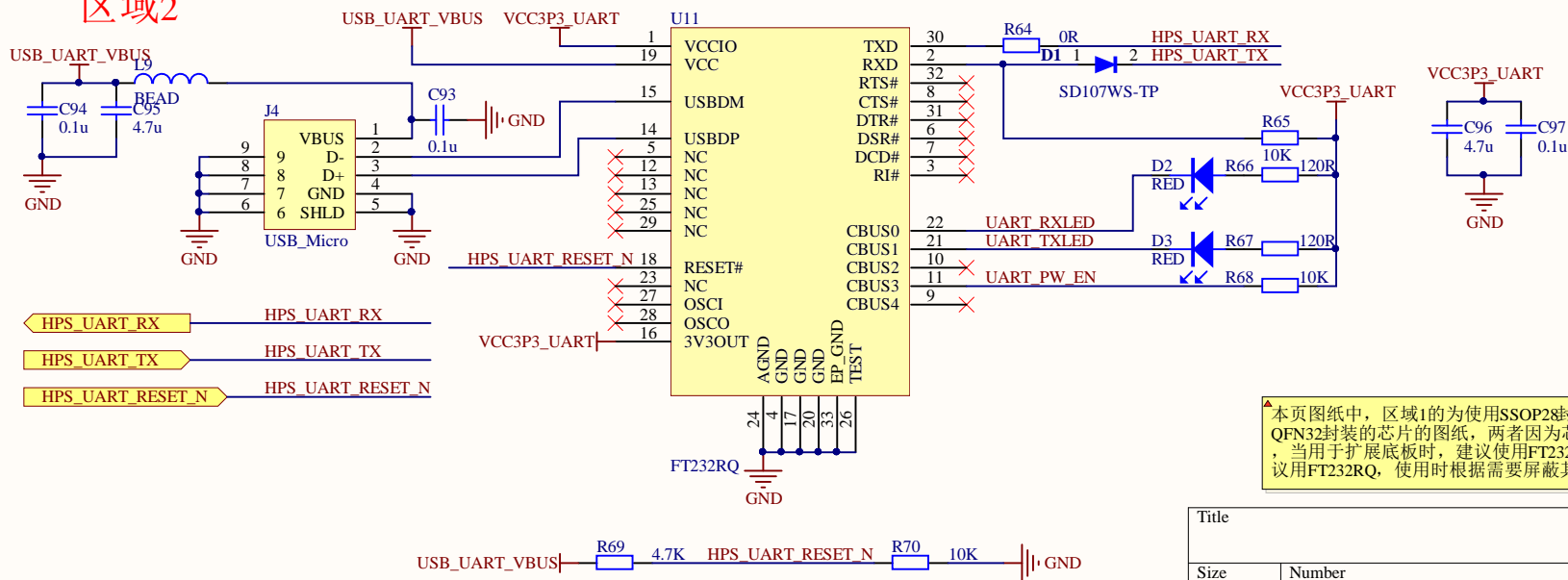
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## 区域1



## 区域2



本页图纸中，区域1的为使用SSOP28封装芯片的图纸，区域2为使用QFN32封装的芯片的图纸，两者因为芯片引脚顺序不一样，不兼容，当用于扩展底板时，建议使用FT232RL，当应用于核心板时，建议用FT232RQ，使用时根据需要屏蔽其中之一。

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