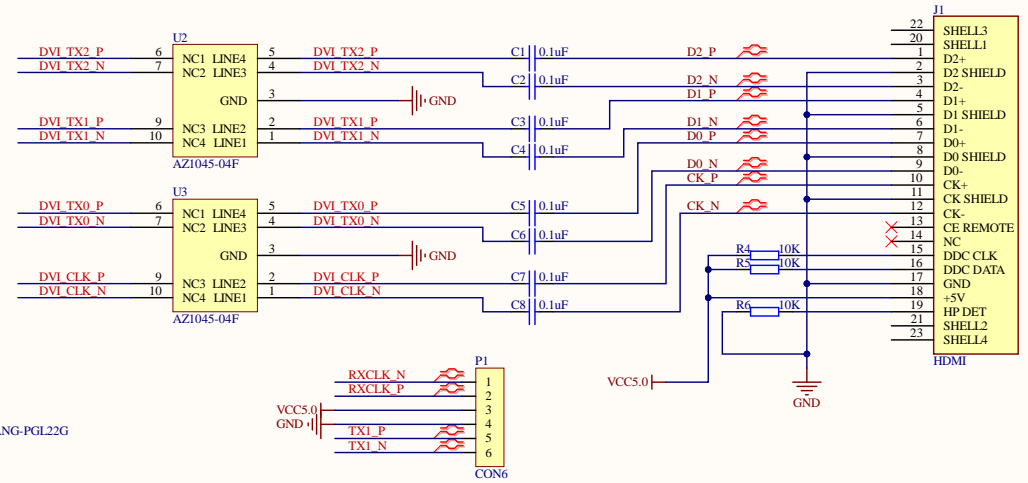
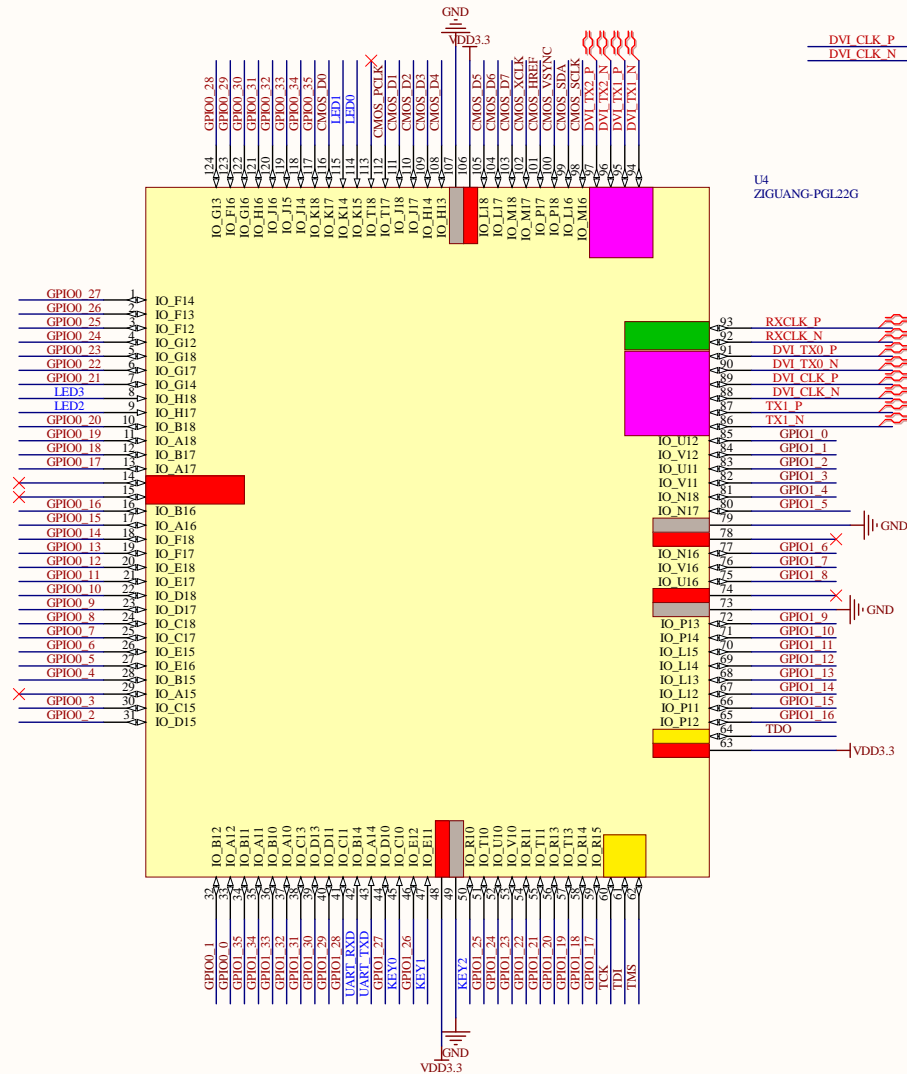
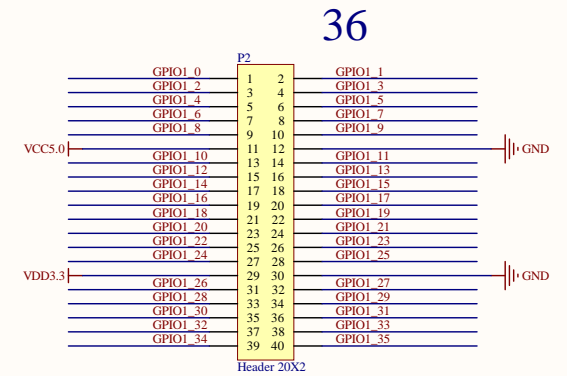


HDMI

紫光核心板



GPIO1



GPI00

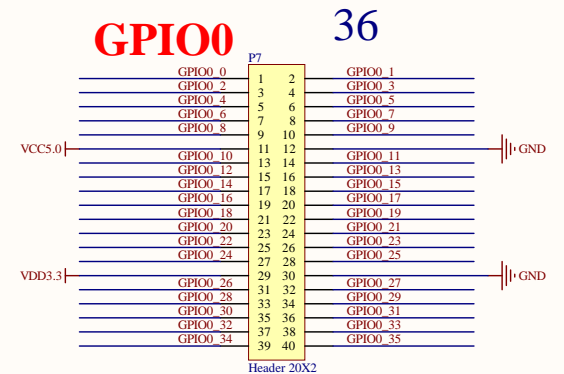


Diagram illustrating the JTAG pin configuration for the J3 connector:

Signal	Pin	Signal	Pin
TCK	1	2	2
TDO	3	4	4
TMS	5	6	6
TDI	7	8	8
TDI	9	10	10

Grounding connections (GND) are indicated for pins 1, 2, 5, 6, 9, and 10. Power supply connections (VDD3.3) are indicated for pins 3 and 4.

Title		
Size A3	Number	Revision
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