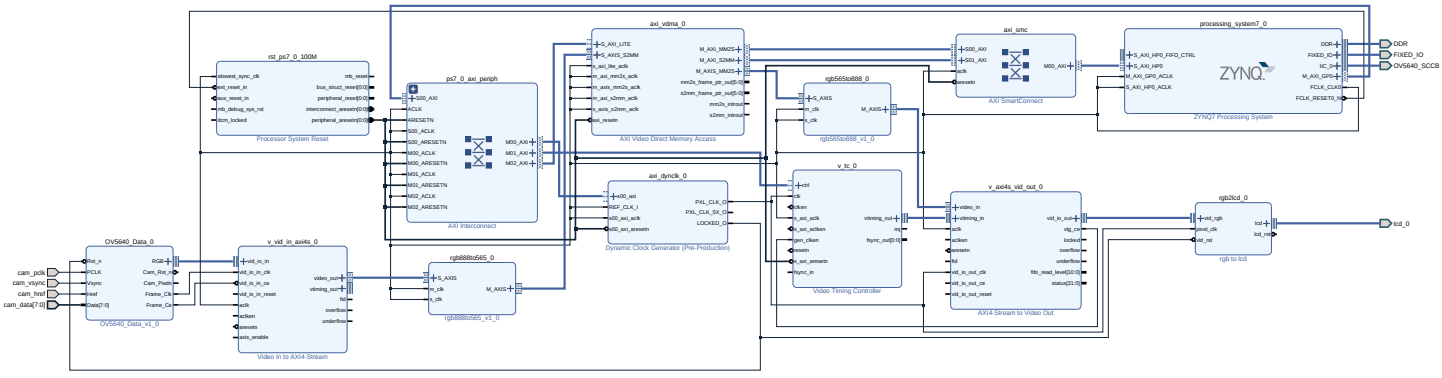
rst_ps7_0_100M
Processor System Reset
ps7_0_axi_periph
AXI Interconnect
axi_vdma_0
AXI Video Direct Memory Access
axi_smc
AXI SmartConnect
processing_system7_0
ZYNQ7 Processing System
rgb565to888_0
rgb565to888_v1_0
v_tc_0
Video Timing Controller
axi_dynclk_0
Dynamic Clock Generator (Pre-Production)
v_axi4s_vid_out_0
AXI4-Stream to Video Out
rgb2lcd_0
rgb to lcd
OV5640_Data_0
OV5640_Data_v1_0
v_vid_in_axi4s_0
Video In to AXI4-Stream
rgb888to565_0
rgb888to565_v1_0
DDR
FIXED_IO
OV5640_SCCB
lcd_0
cam_pclk
cam_vsync
cam_href
cam_data[7:0]