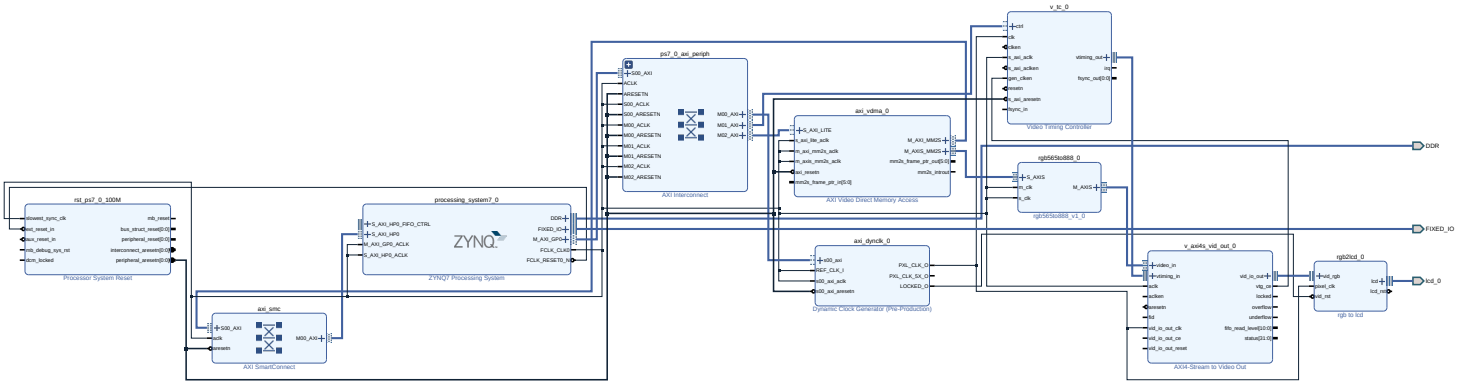
rst_ps7_0_100M
Processor System Reset
processing_system7_0
ZYNQ7 Processing System
axi_smc
AXI SmartConnect
ps7_0_axi_periph
AXI Interconnect
axi_vdma_0
AXI Video Direct Memory Access
v_tc_0
Video Timing Controller
rgb565to888_0
rgb565to888_v1_0
axi_dynclk_0
Dynamic Clock Generator (Pre-Production)
v_axi4s_vid_out_0
AXI4-Stream to Video Out
rgb2lcd_0
rgb to lcd
DDR
FIXED_IO
lcd_0