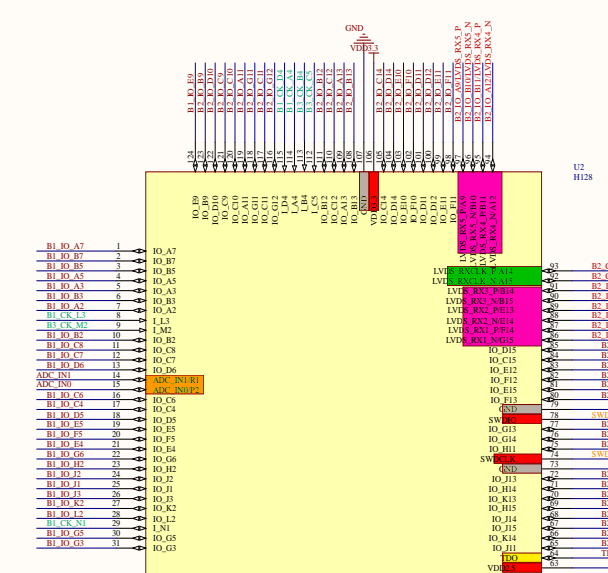
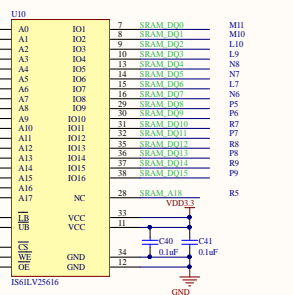


预留 ADC 的 2 个 IO 接口，必要的时候可以引线来作为 ADC 功能



```

B0 IO C3 G3
B0 IO C2 H2
B0 IO J1 J1
B0 IO J2 H1
B0 IO J2 H1
B0 IO B2 B2
B0 IO A2 A2
B0 IO E4 C4
B0 IO B3 B3
B0 IO I2 J2
B0 IO A2 A2
B0 IO A2 A2
B0 IO C3 C3
B0 IO C3 N3
B3 CE M2M2
B3 CE C3 B6
B3 CE D4 D4
PREGA CKR2
B0 IO C3 L3
B3 CE C3 C5
B0 IO C3 C5
B0 IO A3 A3
B0 IO K3 K3
B0 IO R3 R3
B0 IO A3 A3
B0 IO K4 K4
B0 IO J1 J1
B0 IO E4 E4
B0 IO A6 A6
B0 IO A6 A6
B0 IO G6 G6
B0 IO E5 E5
B0 IO E6 E6
B0 IO D6 D6
B0 IO E7 E7
B0 IO C6 C6
B0 IO D6 D6
B0 IO G6 G6
B0 IO A5 A5
B0 IO F8 F8
B0 IO F7 F7
B0 IO B7 B7
B0 IO B7 B7
B0 IO F6 F6
B0 IO A7 A7
B0 IO C6 C6
B0 IO C6 C6
B0 IO G6 G6
B0 IO G6H0

```

UIC		R2
R2-F30	F30	IO.DIFFIN, F33.DIFF, RX, T33P
R2-F31	F31	IO.DIFFIN, F33.DIFF, RX, C33N
R2-F32	C32	IO.DIFFIN, F34.DIFF, RX, T34P
R2-F33	C33	IO.DIFFIN, F34.DIFF, RX, C34N
R2-F34	F34	IO.DIFFIN, F35.DIFF, RX, T35P
R2-F35	F35	IO.DIFFIN, F35.DIFF, RX, C35N
R2-F36	F36	IO.DIFFIN, F36.DIFF, RX, T36P
R2-F37	F37	IO.DIFFIN, F36.DIFF, RX, C36N
R2-F38	F38	IO.DIFFIN, F37.DIFF, RX, T37P
R2-F39	F39	IO.DIFFIN, F37.DIFF, RX, C37N
R2-F40	C40	IO.DIFFIN, F38.DIFF, RX, T38P
R2-F41	C41	IO.DIFFIN, F38.DIFF, RX, C38N
R2-F42	F42	IO.DIFFIN, F39.DIFF, RX, T39P
R2-F43	F43	IO.DIFFIN, F39.DIFF, RX, C39N
R2-F44	F44	IO.DIFFIN, F40.DIFF, RX, T40P
R2-F45	F45	IO.DIFFIN, F40.DIFF, RX, C40N
R2-F46	F46	IO.DIFFIN, F41.DIFF, RX, T41P
R2-F47	F47	IO.DIFFIN, F41.DIFF, RX, C41N
R2-F48	F48	IO.DIFFIN, F42.DIFF, RX, T42P
R2-F49	F49	IO.DIFFIN, F42.DIFF, RX, C42N
R2-F50	F50	IO.DIFFIN, F43.DIFF, RX, T43P
R2-F51	F51	IO.DIFFIN, F43.DIFF, RX, C43N
R2-F52	F52	IO.DIFFIN, F44.DIFF, RX, T44P
R2-F53	F53	IO.DIFFIN, F44.DIFF, RX, C44N
R2-F54	F54	IO.DIFFIN, F45.DIFF, RX, T45P
R2-F55	F55	IO.DIFFIN, F45.DIFF, RX, C45N
R2-F56	F56	IO.DIFFIN, F46.DIFF, RX, T46P
R2-F57	F57	IO.DIFFIN, F46.DIFF, RX, C46N
R2-F58	F58	IO.DIFFIN, F47.DIFF, RX, T47P
R2-F59	F59	IO.DIFFIN, F47.DIFF, RX, C47N
R2-F60	F60	IO.DIFFIN, F48.DIFF, RX, T48P
R2-F61	F61	IO.DIFFIN, F48.DIFF, RX, C48N
R2-F62	F62	IO.DIFFIN, F49.DIFF, RX, T49P
R2-F63	F63	IO.DIFFIN, F49.DIFF, RX, C49N
R2-F64	F64	IO.DIFFIN, F50.DIFF, RX, T50P
R2-F65	F65	IO.DIFFIN, F50.DIFF, RX, C50N
R2-F66	F66	IO.DIFFIN, F51.DIFF, RX, T51P
R2-F67	F67	IO.DIFFIN, F51.DIFF, RX, C51N
R2-F68	F68	IO.DIFFIN, F52.DIFF, RX, T52P
R2-F69	F69	IO.DIFFIN, F52.DIFF, RX, C52N
R2-F70	F70	IO.DIFFIN, F53.DIFF, RX, T53P
R2-F71	F71	IO.DIFFIN, F53.DIFF, RX, C53N
R2-F72	F72	IO.DIFFIN, F54.DIFF, RX, T54P
R2-F73	F73	IO.DIFFIN, F54.DIFF, RX, C54N
R2-F74	F74	IO.DIFFIN, F55.DIFF, RX, T55P
R2-F75	F75	IO.DIFFIN, F55.DIFF, RX, C55N

[illegible]

	BI_IO_A2	7	IO_A2
	BI_CK_L1	8	L13
	B3_CK_M2	9	M2
	BI_IO_B2	10	IO_B2
	BI_IO_C8	11	IO_C8
	BI_IO_C7	12	IO_C7
	BI_IO_D6	13	IO_D6
	ADC_IN1	14	ADC_IN1
	ADC_INP	15	ADC_INP
	BI_IO_C6	16	IO_C6
	BI_IO_C4	17	IO_C4
	BI_IO_D5	18	IO_D5
	BI_IO_E3	19	IO_E3
	BI_IO_F3	20	IO_F3
	BI_IO_F4	21	IO_F4
	BI_IO_G6	22	IO_G6
	BI_IO_H2	23	IO_H2
	BI_IO_J2	24	IO_J2
	BI_IO_J1	25	IO_J1
	BI_IO_K3	26	IO_K3
	BI_IO_K2	27	IO_K2
	BI_IO_L2	28	IO_L2
	BI_CK_N1	29	N1
	BI_IO_G5	30	IO_G5
	BI_IO_G3	31	IO_G3

Pin	Symbol	Function
1	VDD3	3.3V supply
2	GNP	Ground
3	B1 IO S5	IO S5
4	B1 IO S4	IO S4
5	B1 CS N10	CS N10
6	B1 IO S3	IO S3
7	B1 IO S6	IO S6
8	B1 IO S7	IO S7
9	B1 CS P11	CS P11
10	B1 IO S8	IO S8
11	B1 IO S9	IO S9
12	B1 CS P12	CS P12
13	B1 CS P13	CS P13
14	B1 IO S1	IO S1
15	B1 IO S2	IO S2
16	B1 IO S4A	IO S4A
17	B1 IO S2	IO S2
18	B1 CS M15	CS M15
19	B1 IO S15	IO S15
20	B1 IO S9	IO S9
21	B1 CS P14	CS P14
22	B1 IO S10	IO S10
23	B1 CS P15	CS P15
24	B1 CS P16	CS P16
25	B1 CS P17	CS P17
26	B1 CS P18	CS P18
27	B1 CS P19	CS P19
28	B1 CS P20	CS P20
29	B1 CS P21	CS P21
30	B1 CS P22	CS P22
31	B1 CS P23	CS P23
32	B1 CS P24	CS P24
33	B1 CS P25	CS P25
34	B1 CS P26	CS P26
35	B1 CS P27	CS P27
36	B1 CS P28	CS P28
37	B1 CS P29	CS P29
38	B1 CS P30	CS P30
39	B1 CS P31	CS P31
40	B1 CS P32	CS P32
41	B1 CS P33	CS P33
42	B1 CS P34	CS P34
43	B1 CS P35	CS P35
44	B1 CS P36	CS P36
45	B1 CS P37	CS P37
46	B1 CS P38	CS P38
47	B1 CS P39	CS P39
48	B1 CS P40	CS P40
49	B1 CS P41	CS P41
50	B1 CS P42	CS P42
51	B1 CS P43	CS P43
52	B1 CS P44	CS P44
53	B1 CS P45	CS P45
54	B1 CS P46	CS P46
55	B1 CS P47	CS P47
56	B1 CS P48	CS P48
57	B1 CS P49	CS P49
58	B1 CS P50	CS P50
59	B1 CS P51	CS P51
60	B1 CS P52	CS P52
61	B1 CS P53	CS P53
62	B1 CS P54	CS P54
63	B1 CS P55	CS P55
64	B1 CS P56	CS P56
65	B1 CS P57	CS P57
66	B1 CS P58	CS P58
67	B1 CS P59	CS P59
68	B1 CS P60	CS P60
69	B1 CS P61	CS P61
70	B1 CS P62	CS P62
71	B1 CS P63	CS P63
72	B1 CS P64	CS P64
73	B1 CS P65	CS P65
74	B1 CS P66	CS P66
75	B1 CS P67	CS P67
76	B1 CS P68	CS P68
77	B1 CS P69	CS P69
78	B1 CS P70	CS P70
79	B1 CS P71	CS P71
80	B1 CS P72	CS P72
81	B1 CS P73	CS P73
82	B1 CS P74	CS P74
83	B1 CS P75	CS P75
84	B1 CS P76	CS P76
85	B1 CS P77	CS P77
86	B1 CS P78	CS P78
87	B1 CS P79	CS P79
88	B1 CS P80	CS P80
89	B1 CS P81	CS P81
90	B1 CS P82	CS P82
91	B1 CS P83	CS P83
92	B1 CS P84	CS P84
93	B1 CS P85	CS P85
94	B1 CS P86	CS P86
95	B1 CS P87	CS P87
96	B1 CS P88	CS P88
97	B1 CS P89	CS P89
98	B1 CS P90	CS P90
99	B1 CS P91	CS P91
100	B1 CS P92	CS P92
101	B1 CS P93	CS P93
102	B1 CS P94	CS P94
103	B1 CS P95	CS P95
104	B1 CS P96	CS P96
105	B1 CS P97	CS P97
106	B1 CS P98	CS P98
107	B1 CS P99	CS P99
108	B1 CS P100	CS P100
109	B1 CS P101	CS P101
110	B1 CS P102	CS P102
111	B1 CS P103	CS P103
112	B1 CS P104	CS P104
113	B1 CS P105	CS P105

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