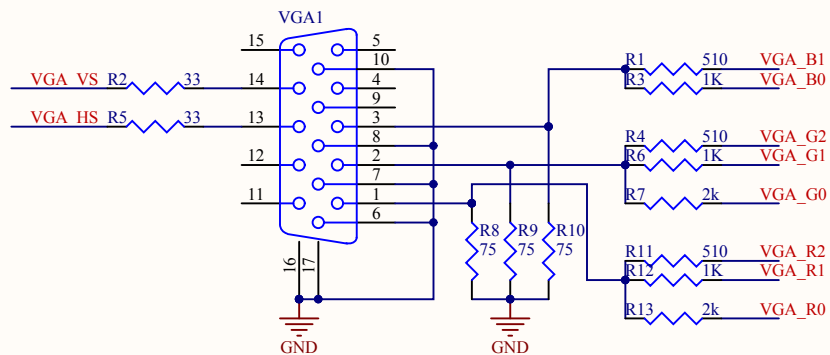
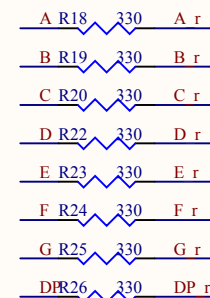
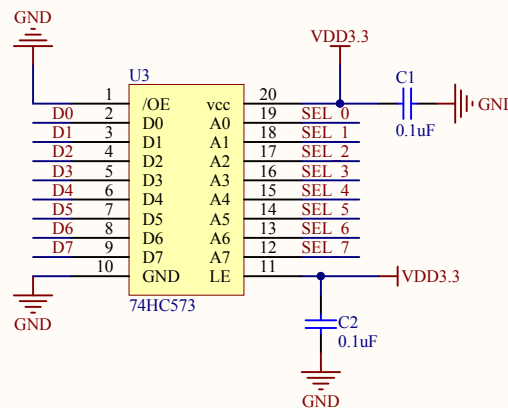
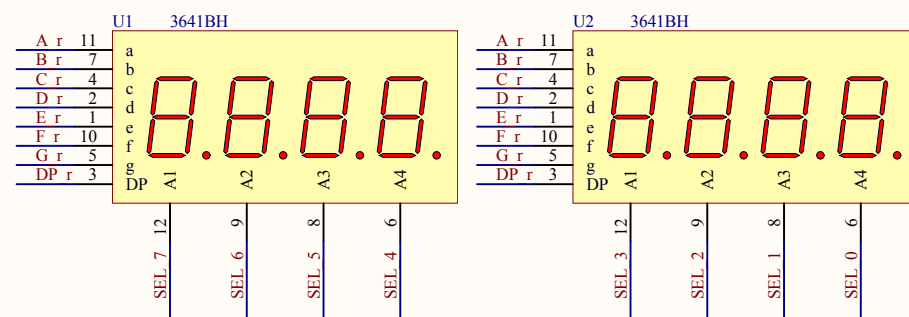


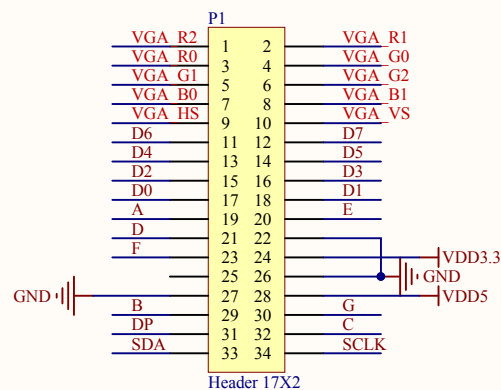
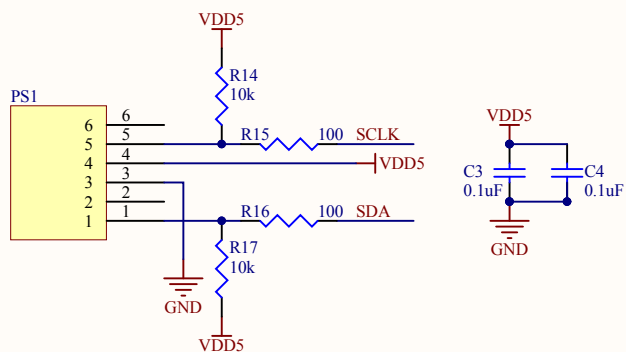
256色VGA接口



7段8位数码管模块



PS2接口



Title		
Size	Number	Revision
A4		
Date:	2015/9/23	Sheet of
File:	F:\XiaoMeige fpga System\seg7 ps2 vga	Sheet of

Bill of Materials

<Parameter Title not found>

Source Data From:	<u>seg7_ps2_vga.PrjPcb</u>
Project:	<u>seg7_ps2_vga.PrjPcb</u>
Variant:	<u>None</u>

Creation Date:	<u>2015/9/23</u>	<u>10:54:54</u>
Print Date:	<u>42270</u>	<u>42270.455</u>

Footprint	Comment	LibRef	Designator	Description	Quantity
CAP0603	0.1uF	CAP	C1, C2, C3, C4	Header, 17-Pin, Dual row	4
Via	RES	RES	GND1, GND2		2
HDR2X17	Header 17X2	Header 17X2	P1		1
PS2	PS2	PS2	PS1		1
RES0603	510	RES	R1, R4, R11		3
RES0603	33	RES	R2, R5		2
RES0603	1K	RES	R3, R6, R12		3
RES0603	2k	RES	R7, R13		2
RES0603	75	RES	R8, R9, R10		3
RES0603	10k	RES	R14, R17		2
RES0603	100	RES	R15, R16		2
RES0603	330	RES	R18, R19, R20, R22, R23, R24, R25, R26		8
	3641BH	3641BH	U1, U2		2
Tssop_20	74HC573	7cHC573	U3		1
DB15M	DB15 母头	DB15	VGA1		1

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Approved	Notes